

Compal Confidential

PEW71_81_91 UMA <LA-6582P> M/B Schematics Document

Intel Arrandale Processor with DDRIII + Ibex Peak-M

2010-06-18

REV:0.2

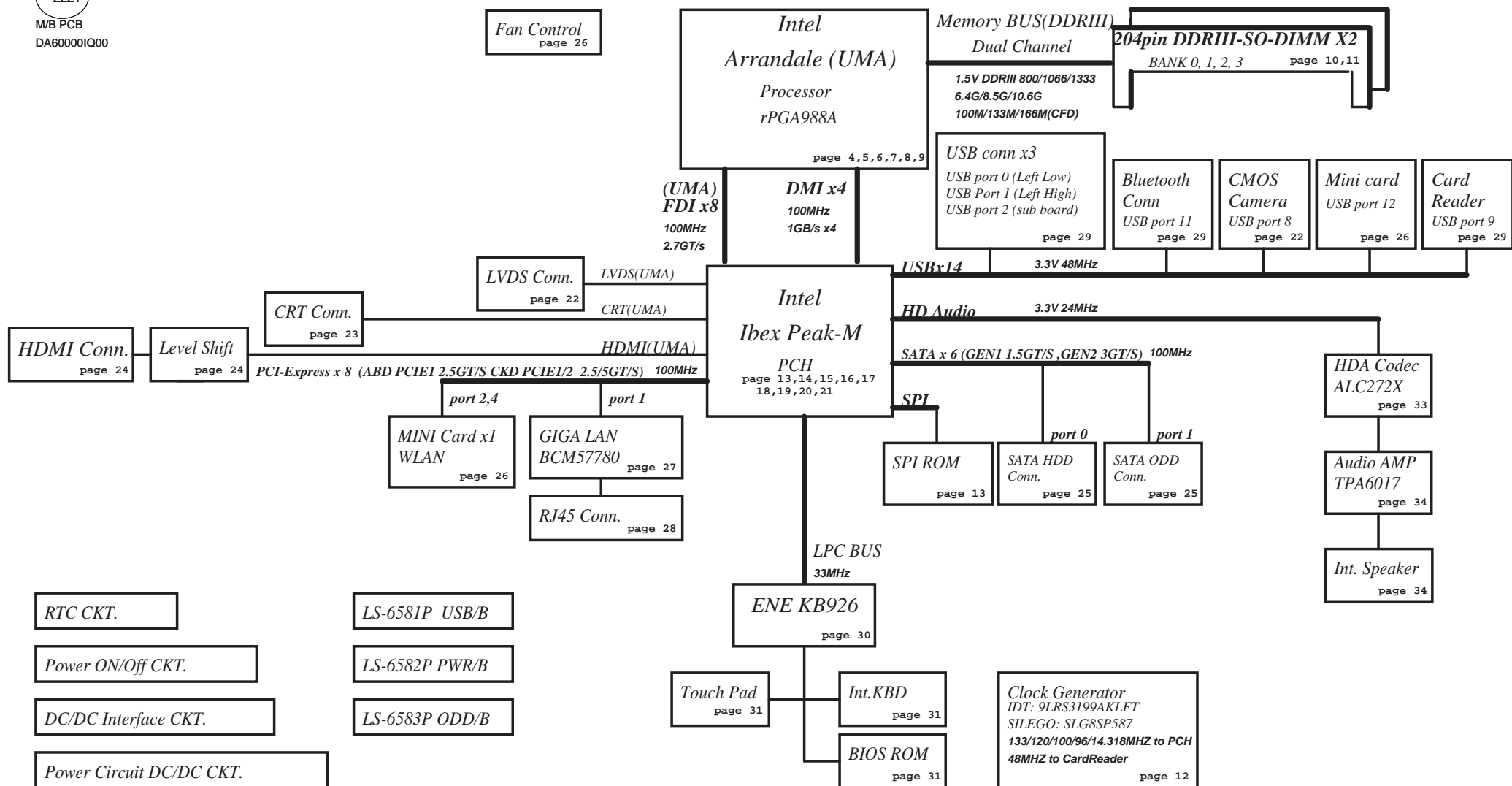
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Model Name PEW71_81 UMA

File Name : LA-6582P

ZZZ1
M/B PCB
DA60000IQ00



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Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	ON	OFF
+0.75VS	0.75V switched power rail for DDR terminator	ON	OFF	OFF
+1.05VS	1.05V switched power rail for PCH	ON	OFF	OFF
+1.05VS_VTT	1.05V switched power rail (1.05 for AUB CPU)	ON	OFF	OFF
+1.5V	1.5V power rail for DDRIII	ON	ON	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V_LAN	3.3V power rail for LAN	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+VSB	VSB always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

External PCI Devices

EC SM Bus1 address

EC SM Bus2 address

Device	Address
Clock Generator (9LRS3199AKLFT, SLG8SP587)	1101 0010b
DDR DIMM0	1001 000Xb
DDR DIMM2	1001 010Xb

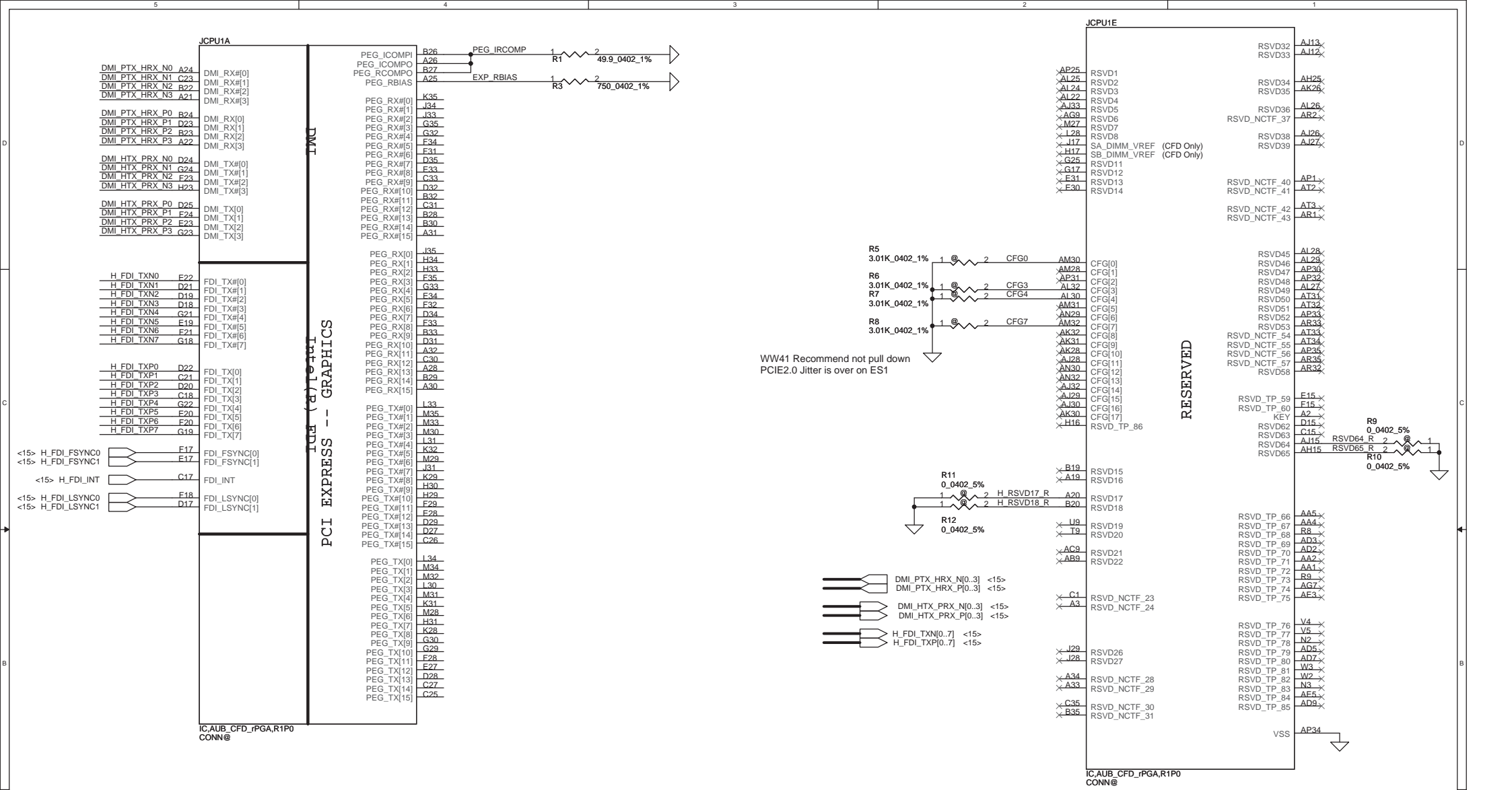
Project ID / Board ID Table for EC-AD channel

Vcc	3.3V +/- 5%					
Ra/Rc	100K +/- 5%					
	Rb / Rd	VAD_BID min	VAD_BID typ	VAD_BID max	Board ID	Project ID
0	0	0 V	0 V	0 V	0.1	Original NEW70/80/90/50/71/91
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V	0.2	PEW71/81/91 Audio Mono/Crystal
2	18K +/- 5%	0.436 V	0.503 V	0.538 V	0.3	
3	33K +/- 5%	0.712 V	0.819 V	0.875 V	1.0	
4	56K +/- 5%	1.036 V	1.185 V	1.264 V		
5	100K +/- 5%	1.453 V	1.650 V	1.759 V		
6	200K +/- 5%	1.935 V	2.200 V	2.341 V		PEW71/81/91 Audio Mono/SUSCLK
7	NC	2.500 V	3.300 V	3.300 V		NEW71/91 Optumis

[illegible]

USB 2.0	USB 1.1	Port	4 External USB Port	3 External USB Port
EHCI1	UHCI0	0	Ext1 Left Low USB	Ext1 Left Low USB
		1	Ext2 Left High USB	Ext2 Left High USB
	UHCI1	2	Ext3 Right USB	Ext3 Right USB
		3		
	UHCI2	4		
		5		
	UHCI3	6		
		7		
EHCI2	UHCI4	8	Camera	Camera
		9	Card Reader	Card Reader
	UHCI5	10		
		11	Blue Tooth	Blue Tooth
	UHCI6	12	1st Min-Card	1st Min-Card
		13		

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eDP Signals Mapping

eDP Singal	PEG Singals	Lane Reversal
eDP_TX0	PEG_HTX_C_GRX_P15	PEG_HTX_C_GRX_P0
eDP_TX#0	PEG_HTX_C_GRX_N15	PEG_HTX_C_GRX_N0
eDP_TX1	PEG_HTX_C_GRX_P14	PEG_HTX_C_GRX_P1
eDP_TX#1	PEG_HTX_C_GRX_N14	PEG_HTX_C_GRX_N1
eDP_TX2	PEG_HTX_C_GRX_P13	PEG_HTX_C_GRX_P2
eDP_TX#2	PEG_HTX_C_GRX_N13	PEG_HTX_C_GRX_N2
eDP_TX3	PEG_HTX_C_GRX_P12	PEG_HTX_C_GRX_P3
eDP_TX#3	PEG_HTX_C_GRX_N12	PEG_HTX_C_GRX_N3
eDP_AUX	PEG_GTX_C_HRX_P13	PEG_GTX_C_HRX_P2
eDP_AUX#	PEG_GTX_C_HRX_N13	PEG_GTX_C_HRX_N2
eDP_HPD#	PEG_GTX_C_HRX_P12	PEG_GTX_C_HRX_P3

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CFG0 - PCI-Express Configuration Select

*1:Single PEG
0:Bifurcation enabled

CFG3 - PCI-Express Static Lane Reversal

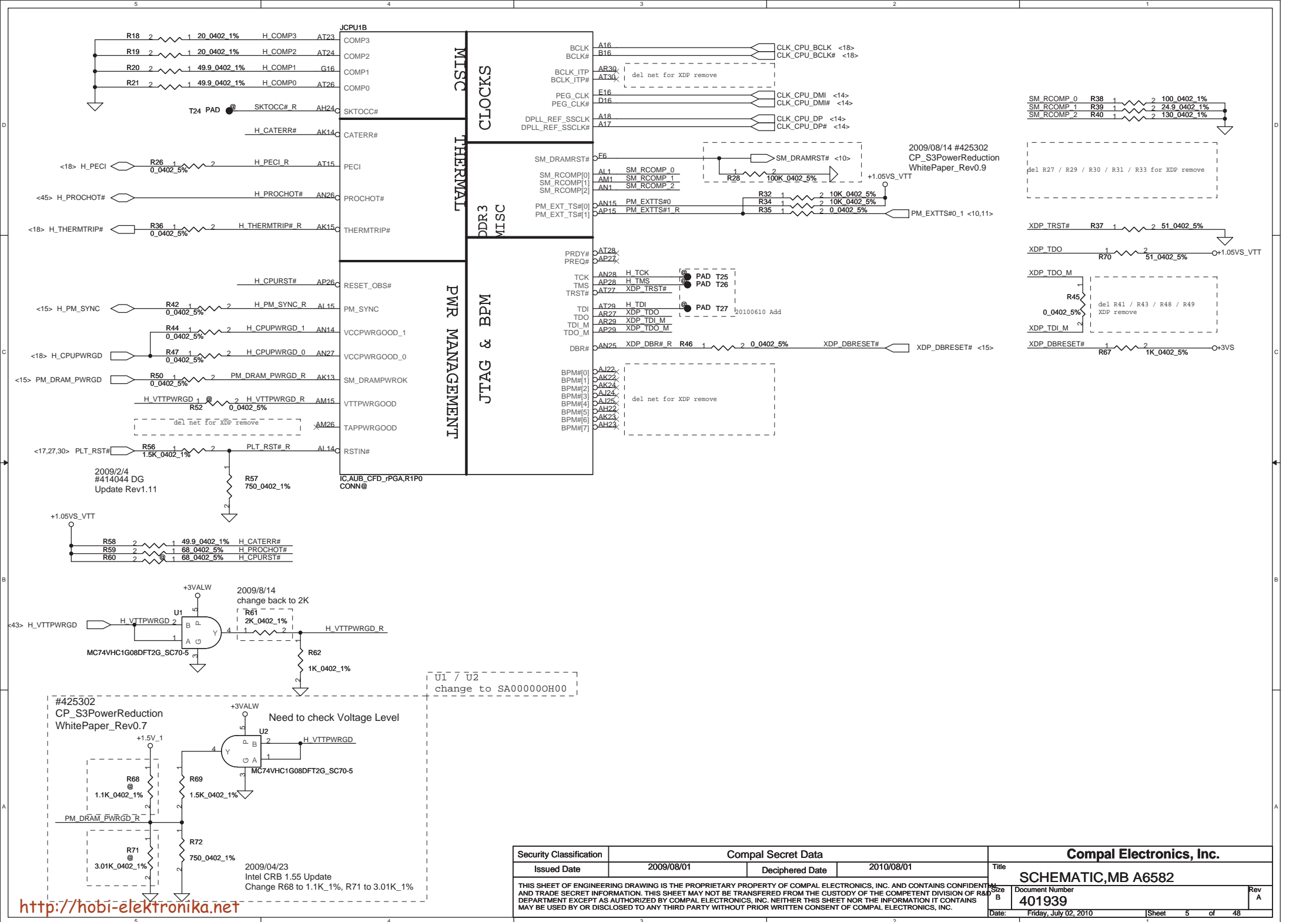
*1 :Normal Operation
0 :Lane Numbers Reversed
15 -> 0, 14 -> 1, ...

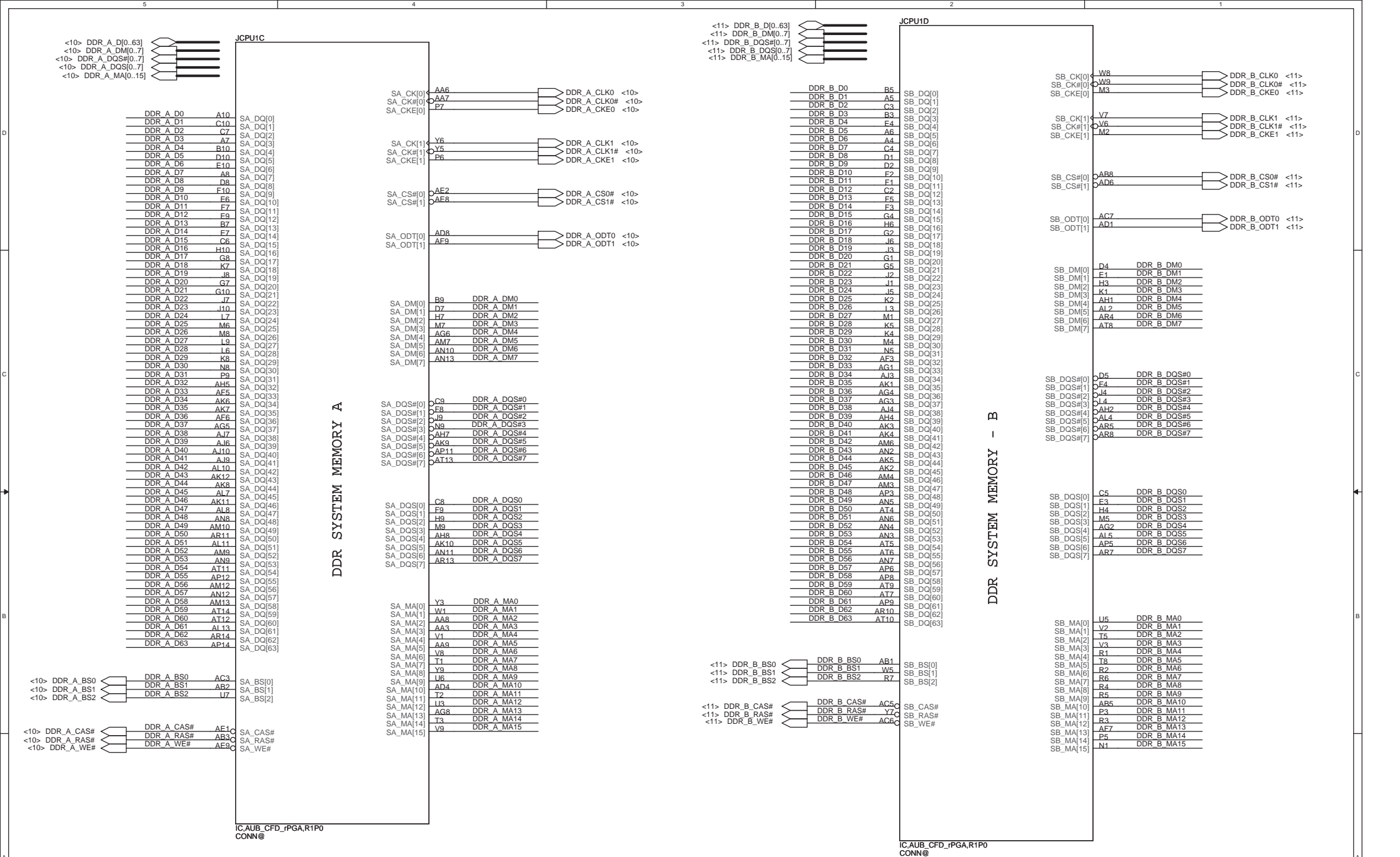
CFG4 - Display Port Presence

*1:Disabled; No Physical Display Port
attached to Embedded Display Port
0:Enabled; An external Display Port
device is connected to the Embedded
Display Port

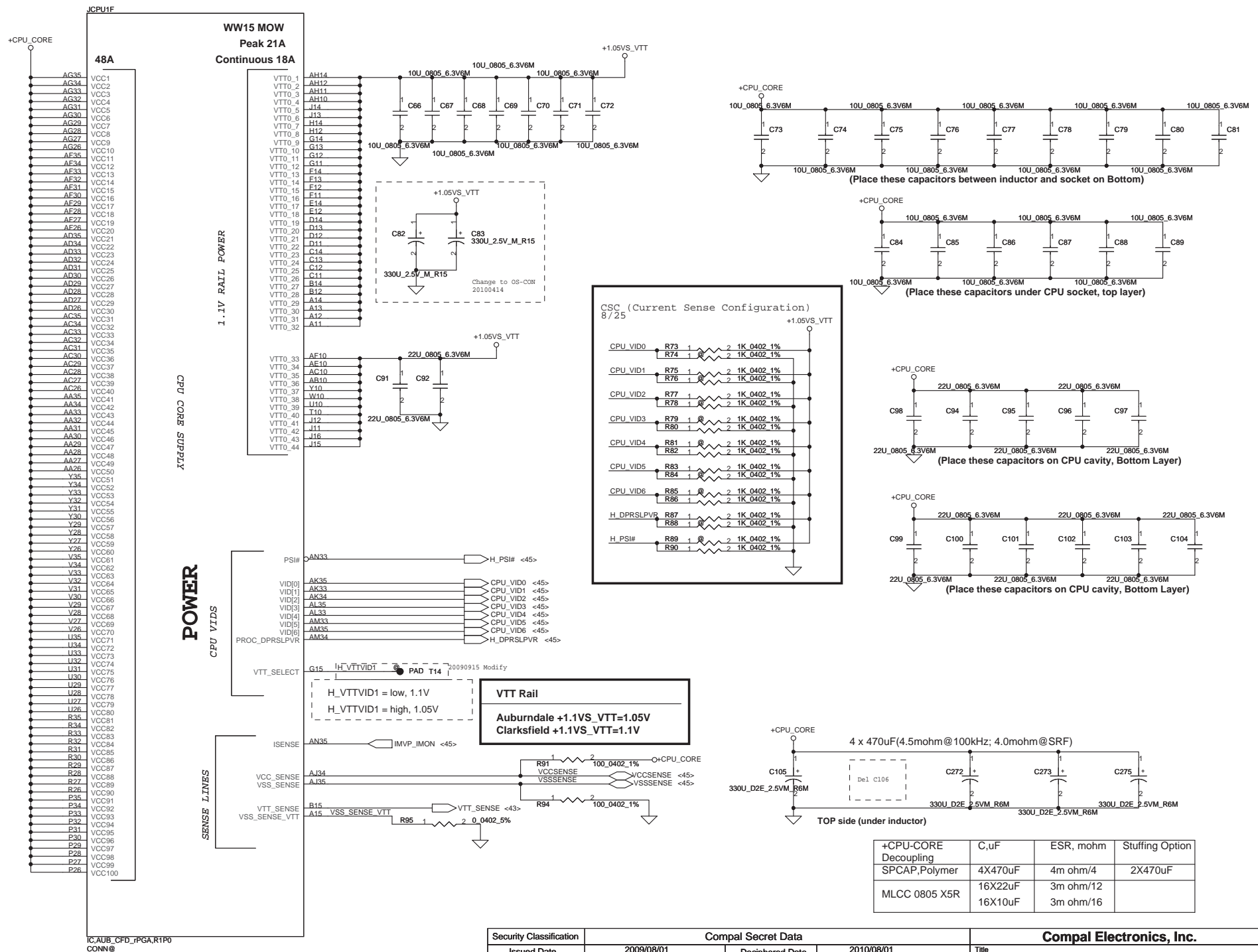
..Default

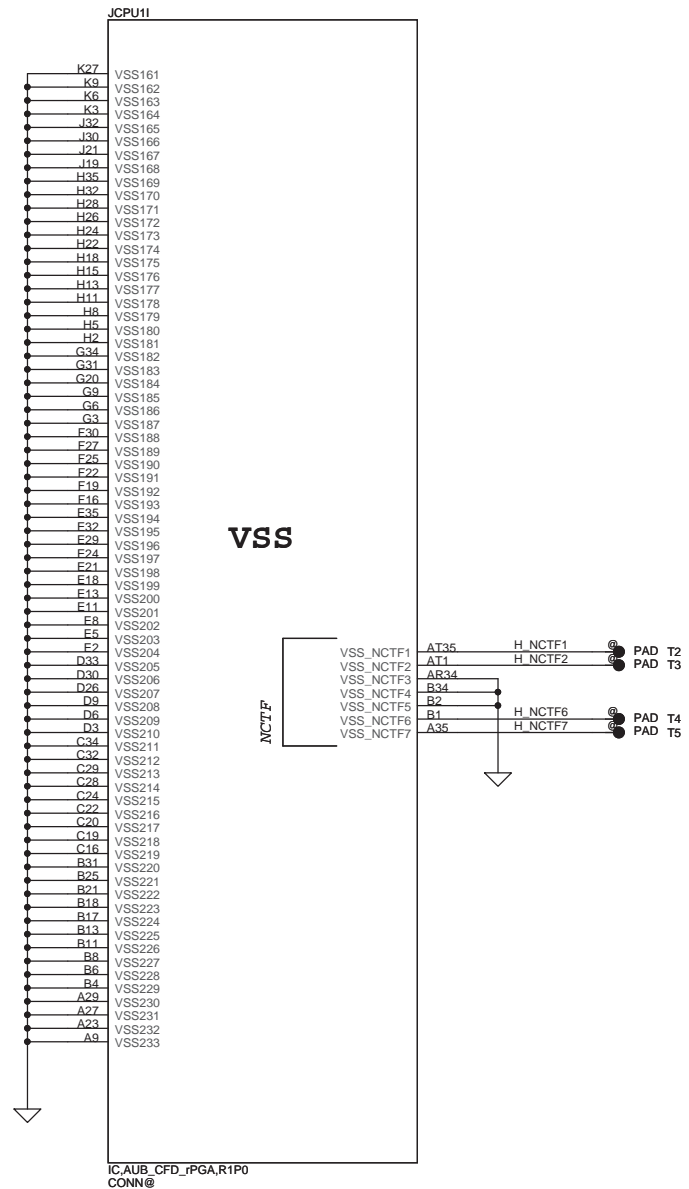
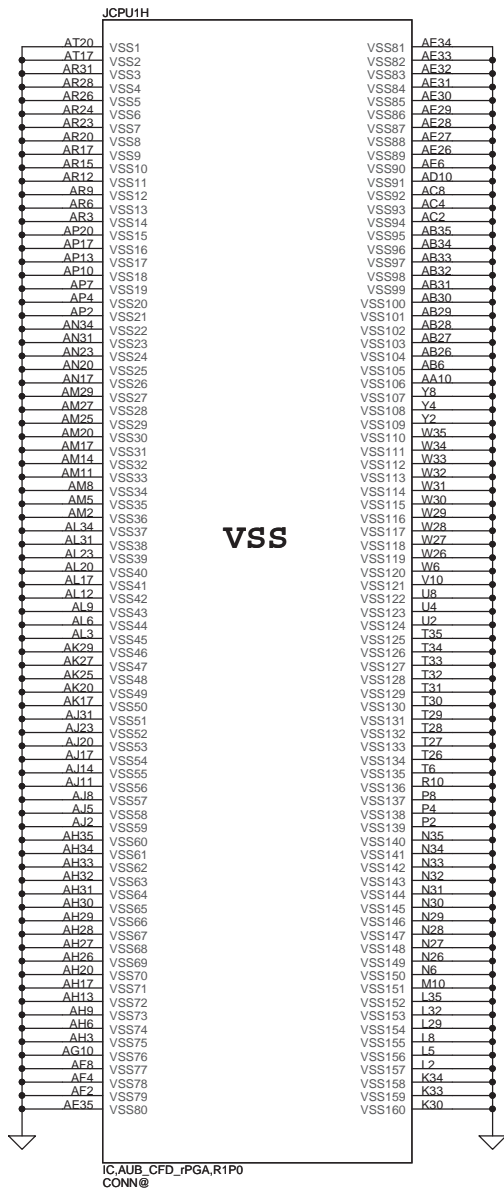
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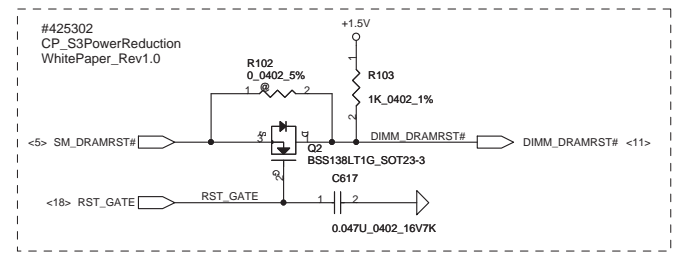
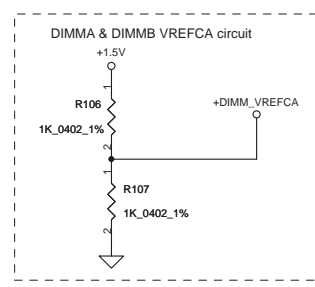
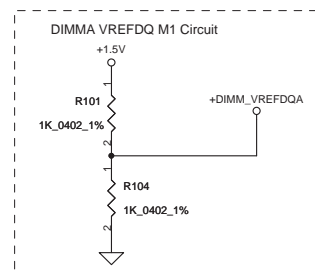


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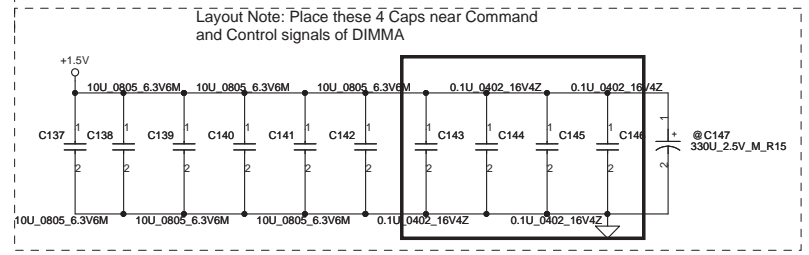




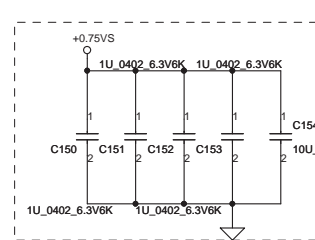
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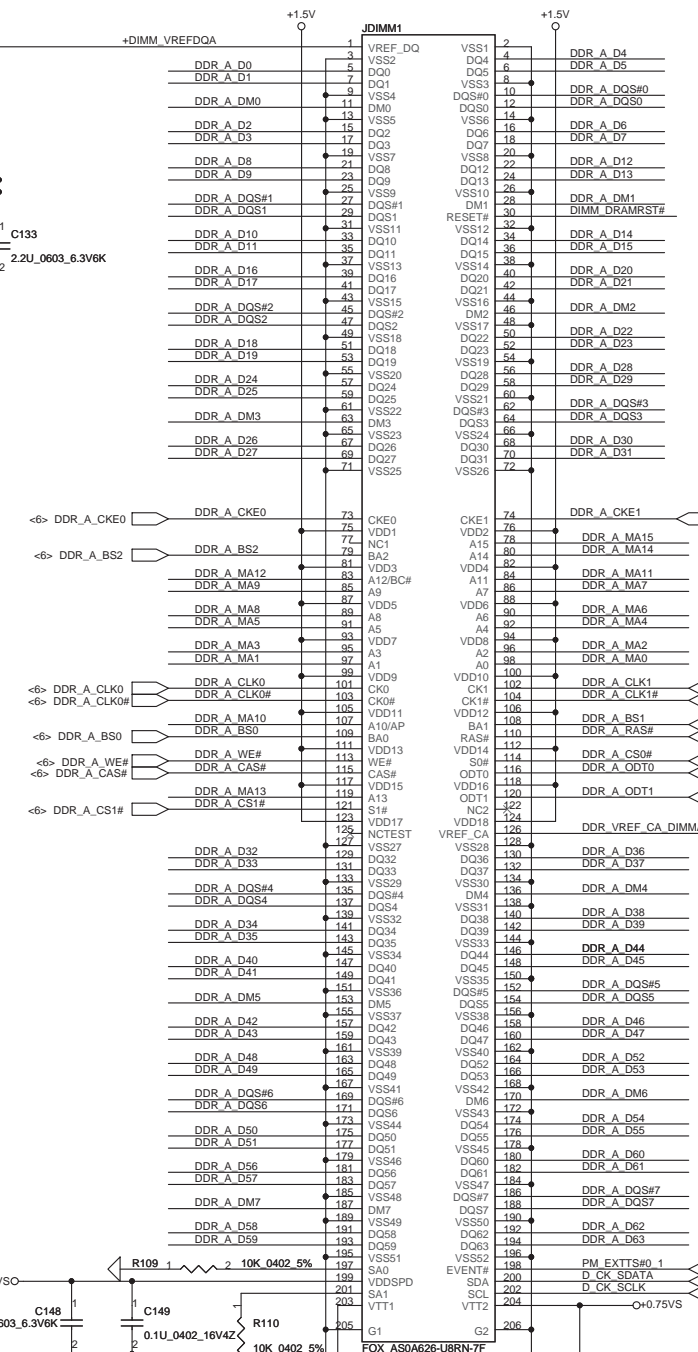
Layout Note:
Place near JDIMM1



Layout Note:
Place near JDIMM1.203 & JDIMM1.204

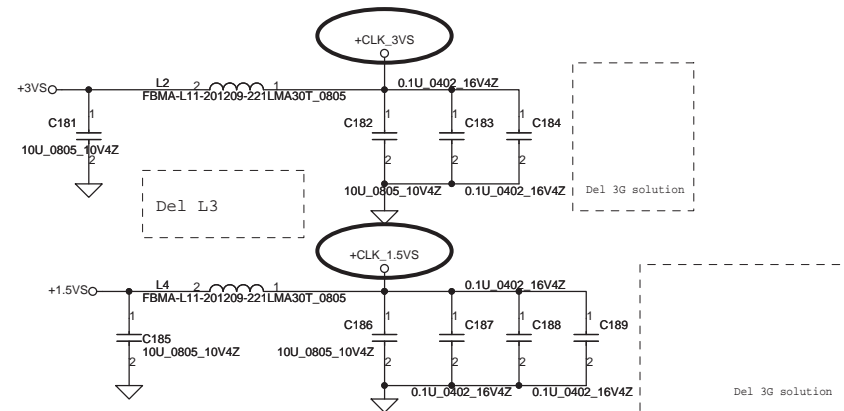
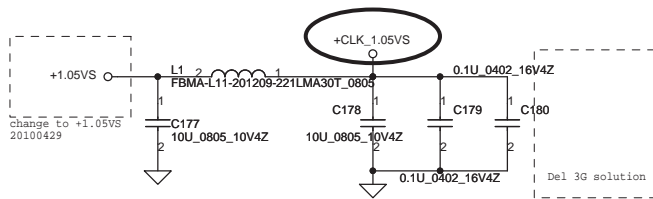


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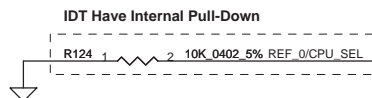
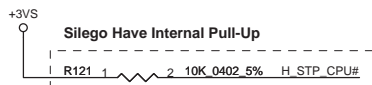
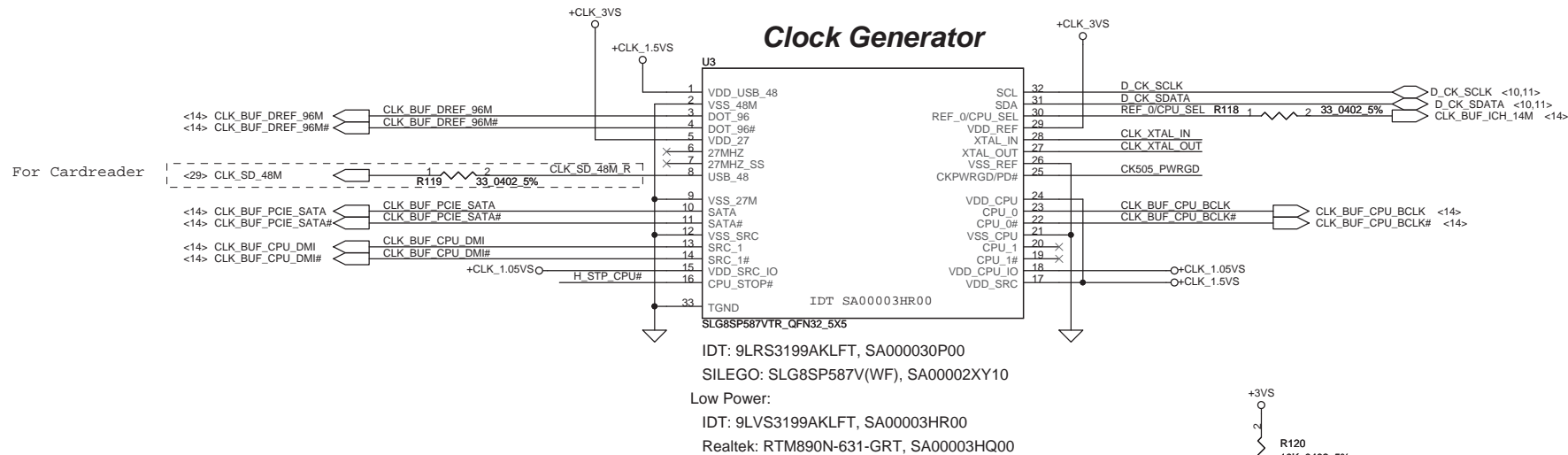


DDR3 SO-DIMM A
Change to Reverse Type
8mm High

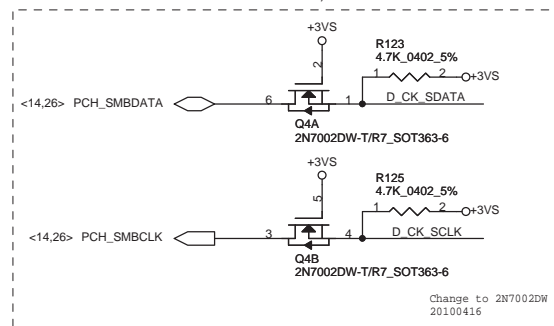
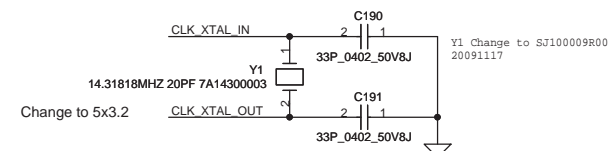
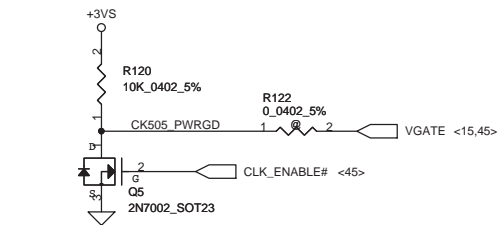
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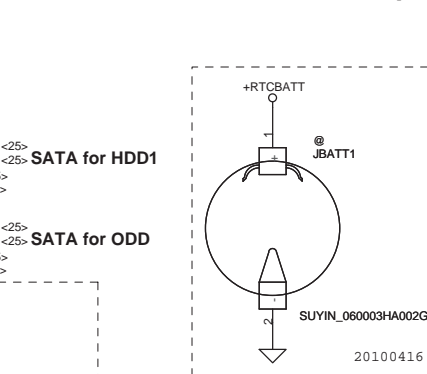
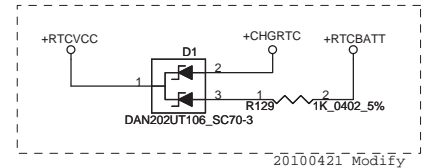
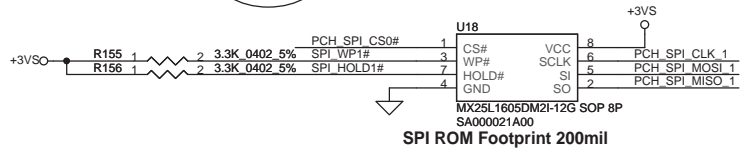
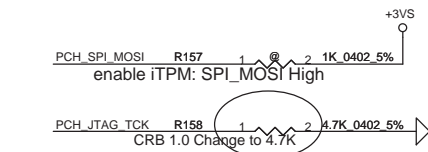
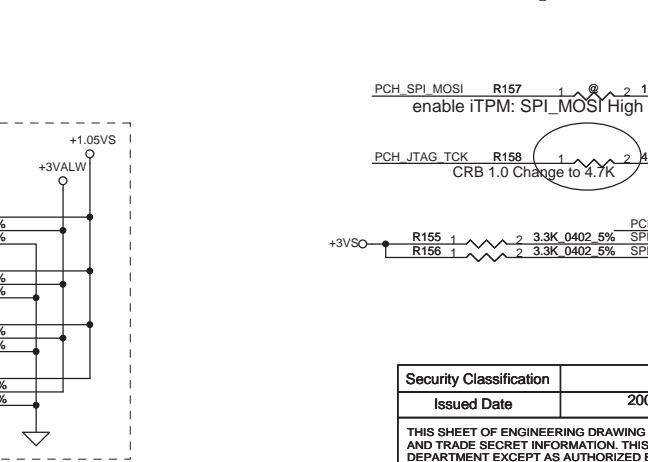
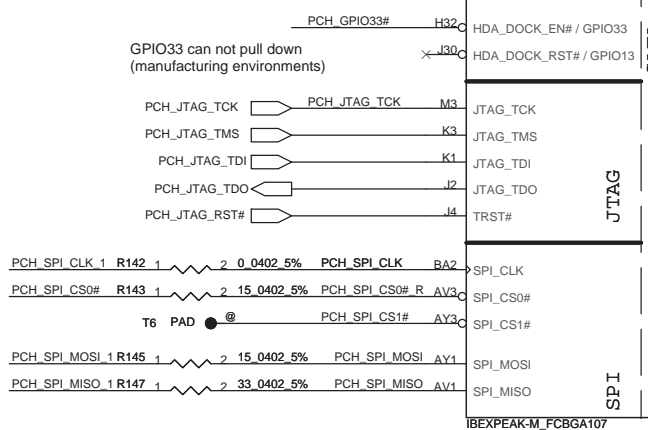
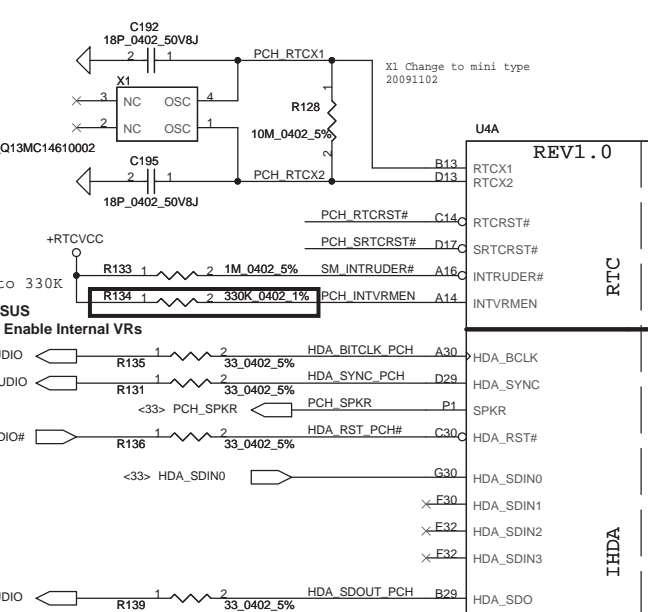
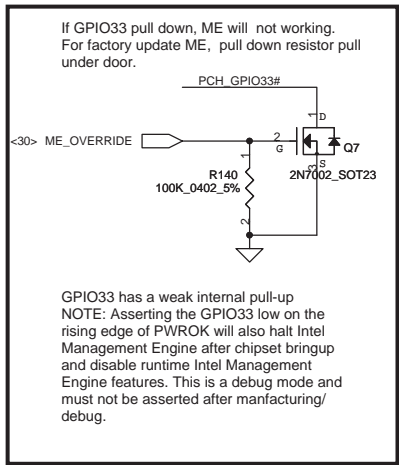
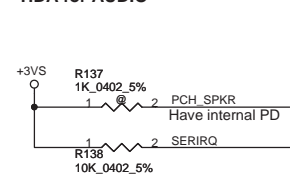
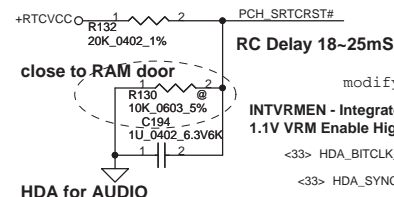
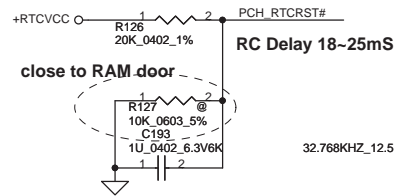
Clock Generator



PIN 30	CPU_0	CPU_1
0 (Default)	133MHz	133MHz
1	100MHz	100MHz



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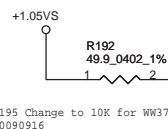
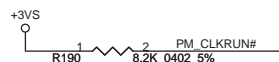


GPIO21	Project
0	NEW50/70/80/90
1	NEW71/91

	GPIO19 PCH_GPIO19	GPIO37 VGA_PRSNT_L#
dGPU	0	0
iGPU	0	1
SG	1	X

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<4> DMI_PTX_HRX_P[0..3] DMI PTX HRX_P[0..3]



U4C
DMI HTX PRX N0 BC24
DMI HTX PRX N1 B122
DMI HTX PRX N2 AW20
DMI HTX PRX N3 B120
DMI HTX PRX P0 BD24
DMI HTX PRX P1 BC22
DMI HTX PRX P2 BA20
DMI HTX PRX P3 BG20
DMI PTX HRX N0 BE22
DMI PTX HRX N1 BF21
DMI PTX HRX N2 BD20
DMI PTX HRX N3 BE18
DMI PTX HRX P0 BD22
DMI PTX HRX P1 BH21
DMI PTX HRX P2 BC20
DMI PTX HRX P3 BD18

DMI0RXN
DMI1RXN
DMI2RXN
DMI3RXN
DMI0RXP
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DMI3TXP
DMI_ZCOMP
DMI_IRCOMP

REV1.0

FDI_RXN0 BA18 H_FDI_TXN0
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FDI_RXN4 BA16 H_FDI_TXN4
FDI_RXN5 BE14 H_FDI_TXN5
FDI_RXN6 BA14 H_FDI_TXN6
FDI_RXN7 BC12 H_FDI_TXN7
FDI_RXP0 BB18 H_FDI_TXP0
FDI_RXP1 BE17 H_FDI_TXP1
FDI_RXP2 BC16 H_FDI_TXP2
FDI_RXP3 BG16 H_FDI_TXP3
FDI_RXP4 AW16 H_FDI_TXP4
FDI_RXP5 BD14 H_FDI_TXP5
FDI_RXP6 BB14 H_FDI_TXP6
FDI_RXP7 BD12 H_FDI_TXP7

FDI_INT BJ14 H_FDI_INT <4>
FDI_FSYNC0 BF13 H_FDI_FSYNC0 <4>
FDI_FSYNC1 BH13 H_FDI_FSYNC1 <4>
FDI_LSYNC0 BJ12 H_FDI_LSYNC0 <4>
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VGATE R198 2 1 0 0402_5%

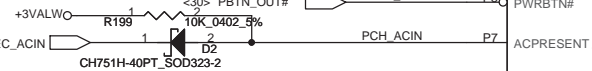
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MEPWROK K5

LAN_RST# A10
LAN_RST#

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DRAMPWROK

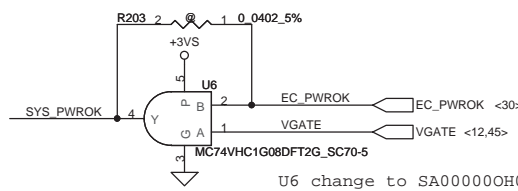
PCH_RSMRST# C16
RSMRST#

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SUS_PWR_ACK



PCH_GPIO72 A6
PCH_GPIO72

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EC_SWI#



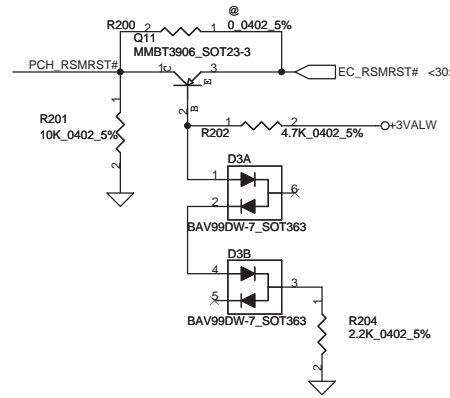
SYS_PWROK R205 1 2 10K_0402_5%
EC_PWROK R206 1 2 10K_0402_5%
LAN_RST# R207 1 2 10K_0402_5%

No used Integrated LAN,
connecting LAN_RST# to GND

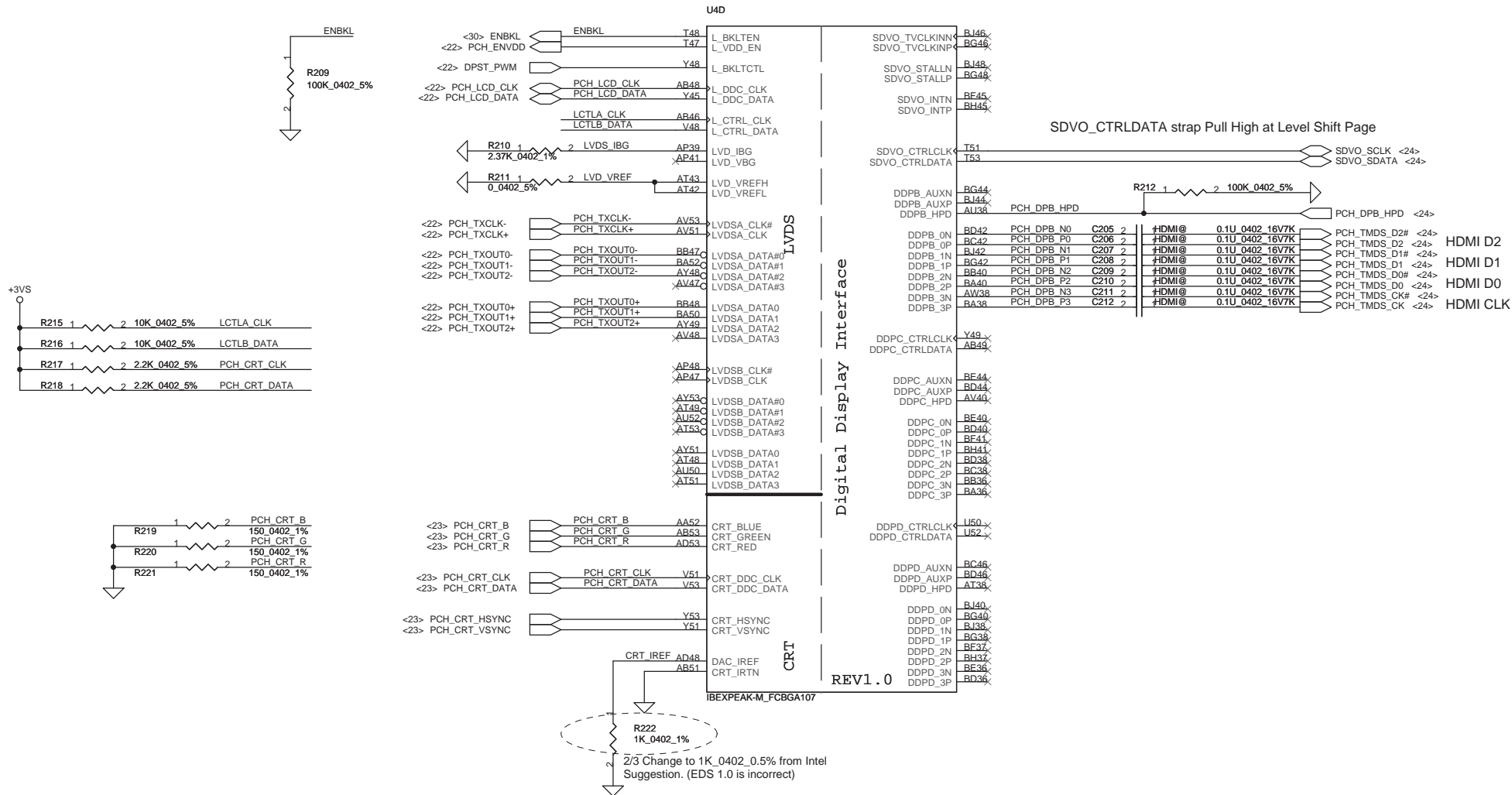
System Power Management

WAKE# J12 PCH_PCIE_WAKE# PCH_PCIE_WAKE# <26,27>
CLKRUN# / GPIO32 Y1 PM_CLKRUN# PM_CLKRUN# <30>
SUS_STAT# / GPIO61 P8 PCH_GPIO61 @ PAD T7
SUSCLK / GPIO62 E3 PCH_SUSCLK <30>
SLP_S5# / GPIO63 E4 PM_SLP_S5# <30>
SLP_S4# H7 PM_SLP_S4# <30>
SLP_S3# E12 PM_SLP_S3# <30>
SLP_M# K8 PM_SLP_M# @ PAD T9
TP23 N2 PM_SLP_DS# @ PAD T10
PMSYNCH BJ10 H_PM_SYNC <5>
SLP_LAN# / GPIO29 F6 PM_SLP_LAN#

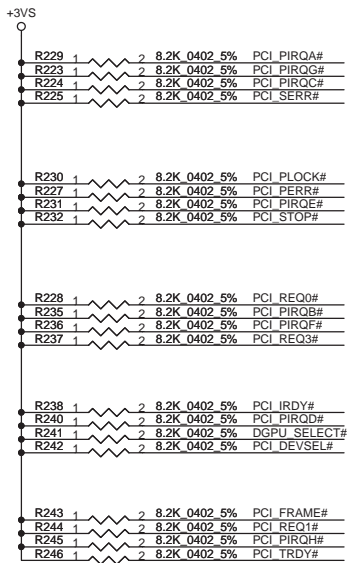
32.768KHZ ouput for remove EC crystal
20091103



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A16 swap override Strap/Top-Block Swap Override jumper

Low=A16 swap override/Top-Block Swap Override enabled High=Default *

T11 PAD @

T12 PAD @

<30> CLK_PCI_LPC
<14> CLK_PCI_FB

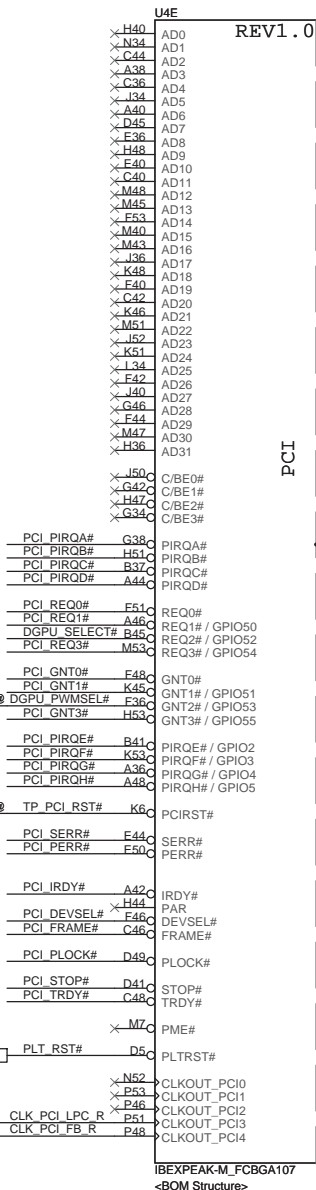
2008/1/6 2009MOW01 change to 22 ohm

Boot BIOS Strap		
PCI_GNT#0	PCI_GNT#1	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

A16 swap override Strap/Top-Block Swap Override jumper

PCI_GNT#3 Low = A16 swap High = Default

http://hobi-elektronika.net



PCI_GNT0# R254 1 2 1K 0402 5%

Have internal PU

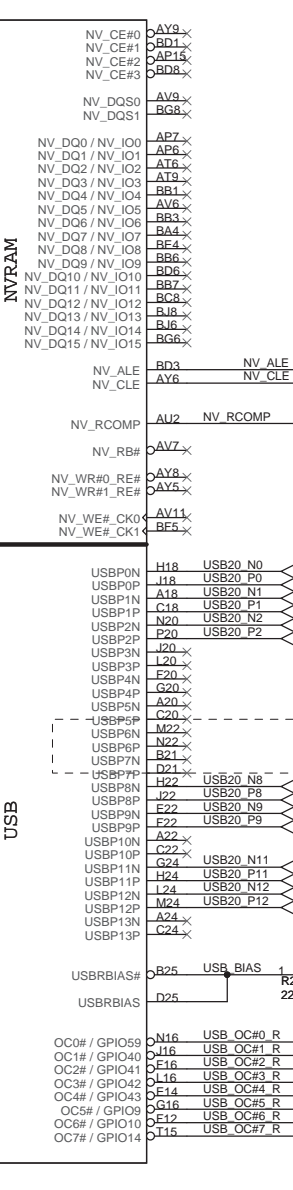
PCI_GNT1# R256 1 2 1K 0402 5%

Have internal PU

PCI_GNT3# R258 1 2 1K 0402 5%

Have internal PU

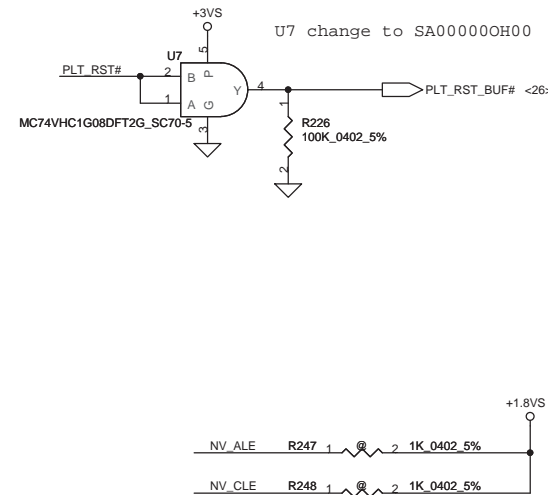
IBEXPEAK-M_FCBGA107
<BOM Structure>



USB/B (Left Side Low)
USB Port (Left Side High)
USB/B (Right Side)

CMOS Camera (LVDS)
Card Reader
Del SIM Card USB
Bluetooth
Mini Card(WLAN)
Del 3G Card USB

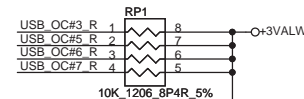
OC[0..3] use for EHCl 1
OC[4..7] use for EHCl 2



Intel Anti-Theft Techonlogy	
NV_ALE	High=Endabled
NV_CLE	Low=Disabale(floating) *

DMI Termination Voltage	
NV_CLE	Set to Vcc when HIGH
NV_CLE	Set to Vss when LOW

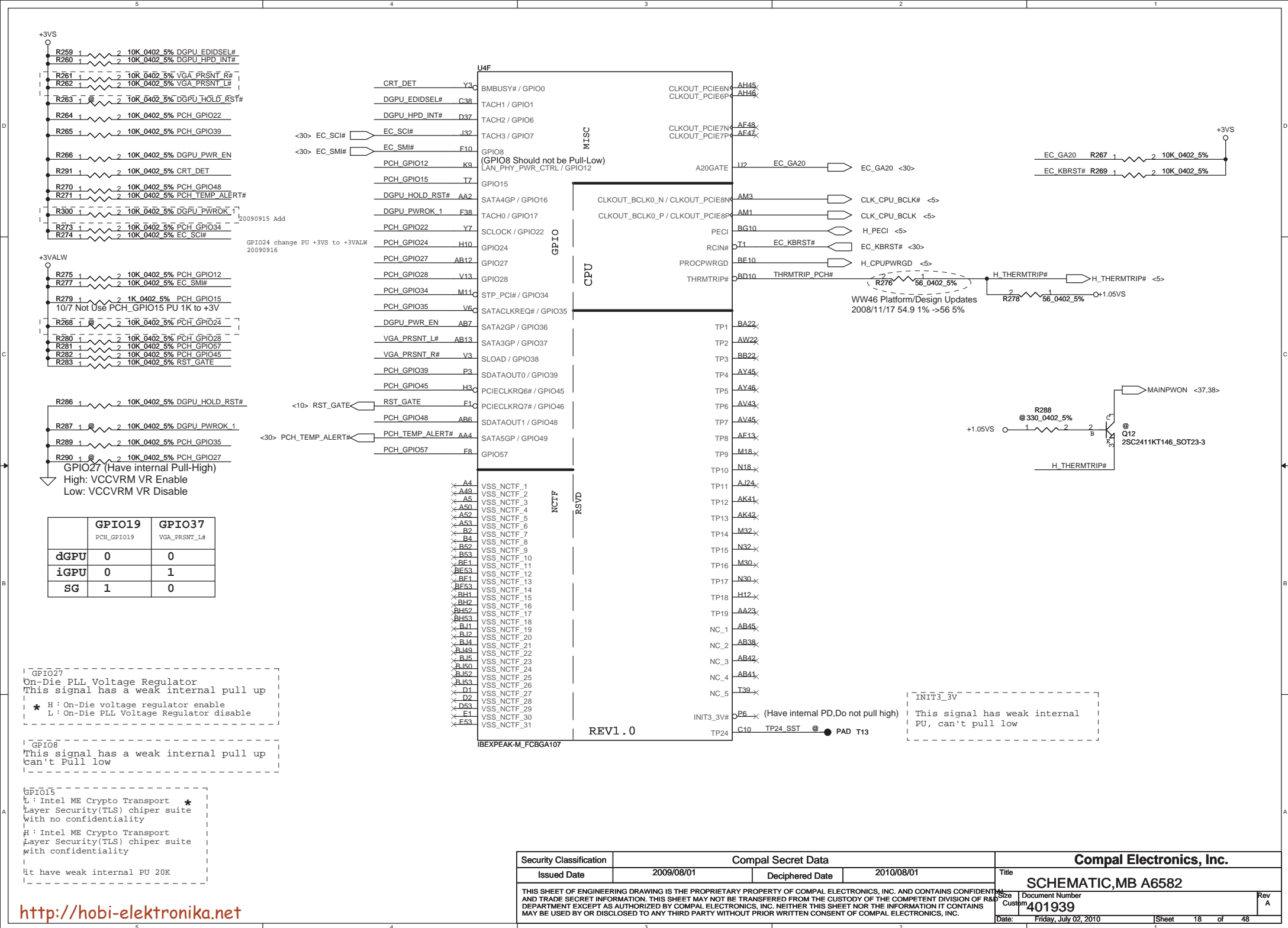
NV_ALE
Enable Intel Anti-Theft
Technology : 8.2K PU to +3VS
Disable Intel Anti-Theft
Technology : floating(internal PD)
NV_CLE
DMI termination voltage.
weak internal PU, don't PD

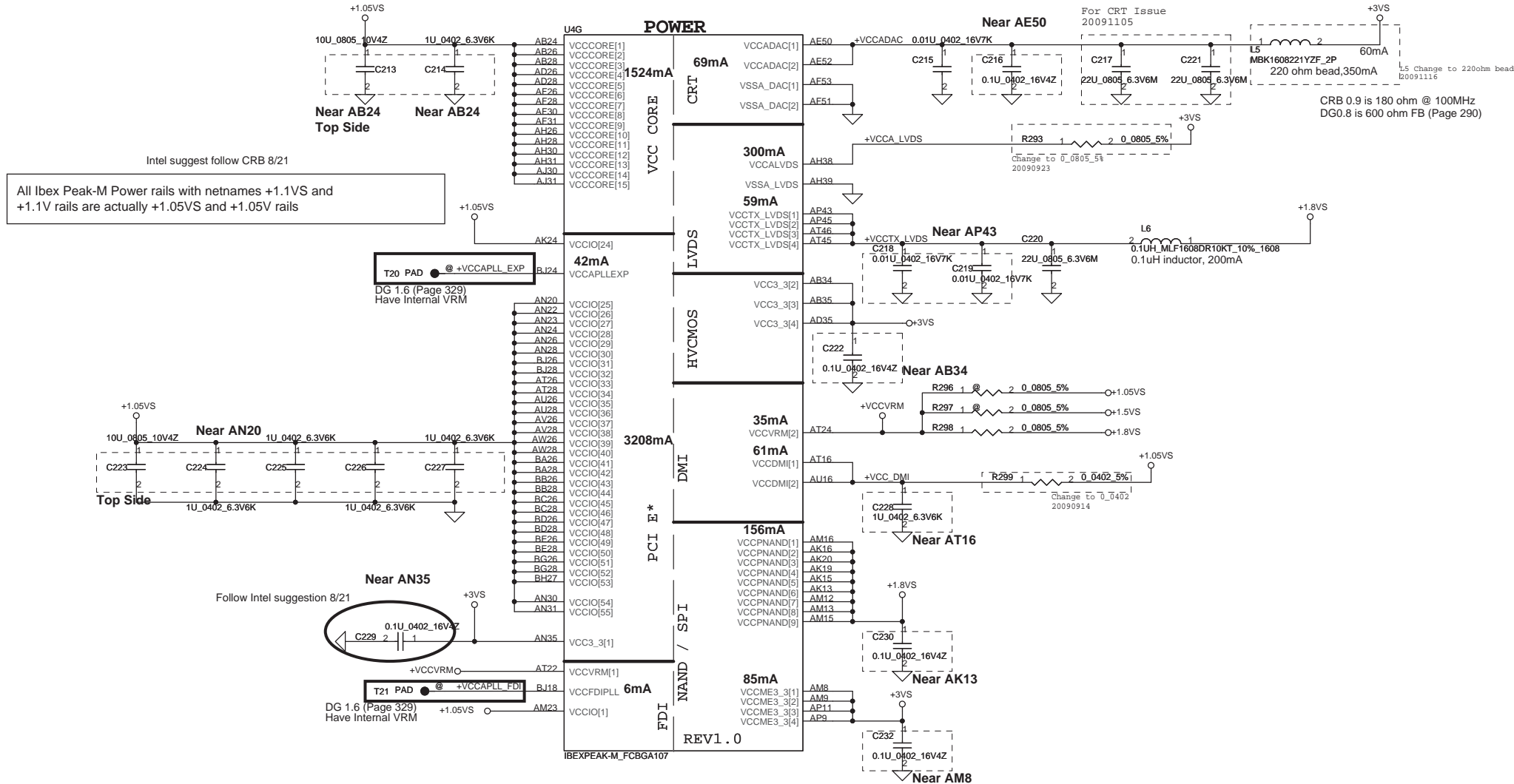


USB_OC#1 R R255 1 2 10K 0402 5%

USB_OC#4 R R257 1 2 10K 0402 5%

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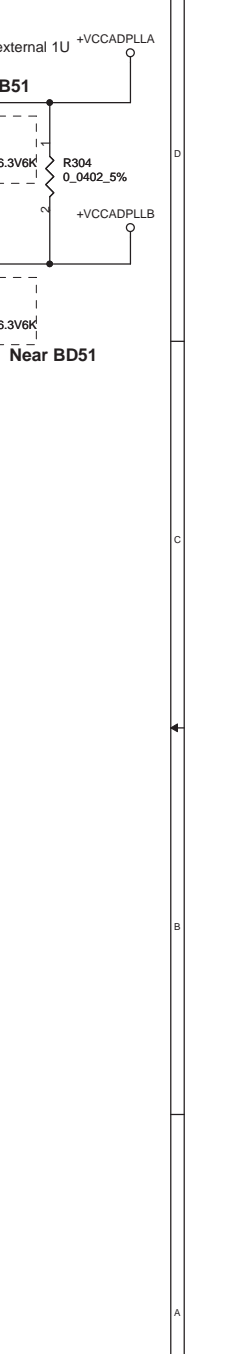
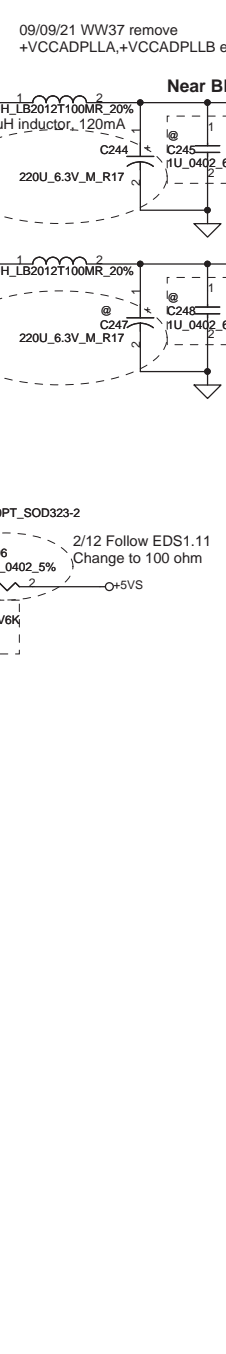
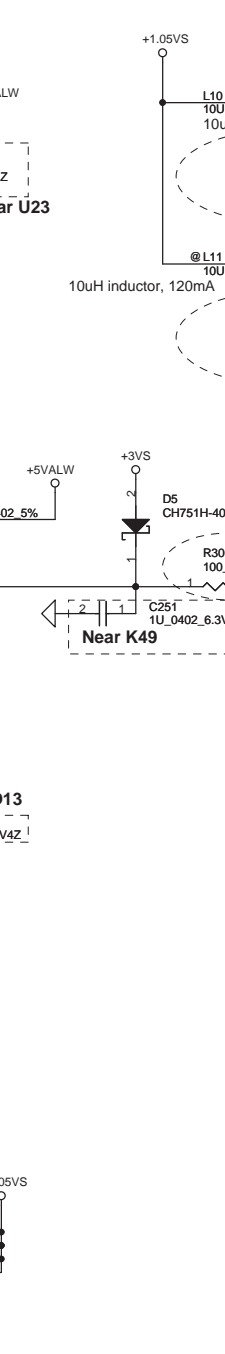
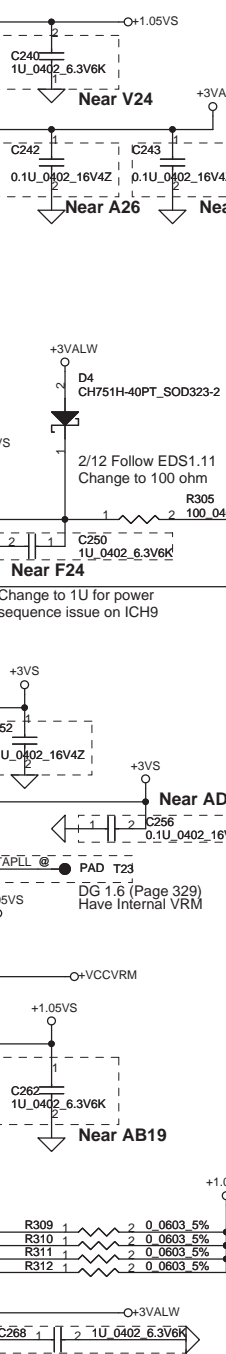
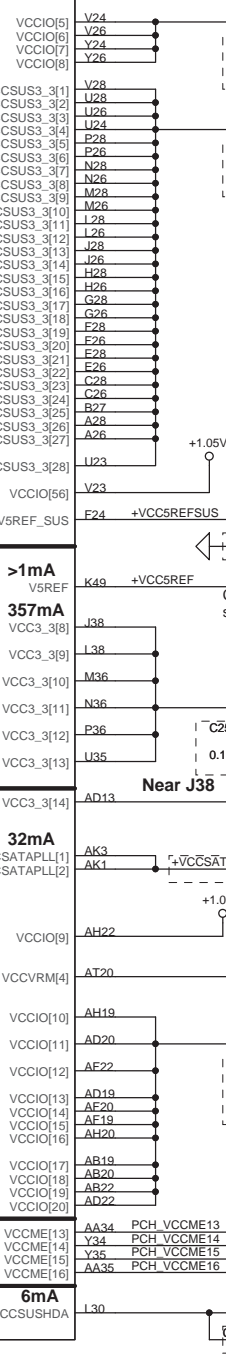
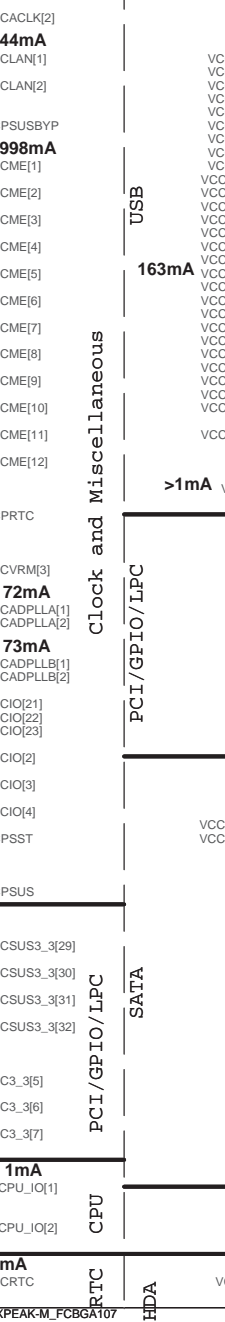


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All Ixex Peak-M Power rails with netnames +1.1VS and +1.1V rails are actually +1.05VS and +1.05V rails

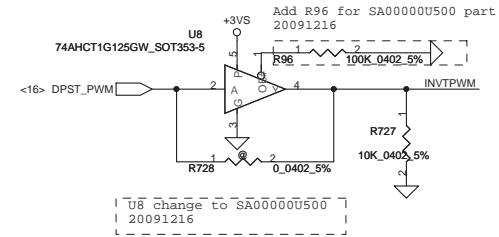
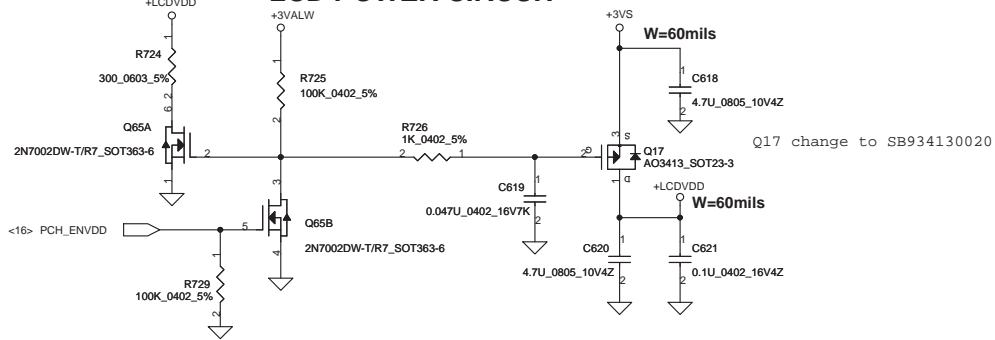
POWER

REV1.0

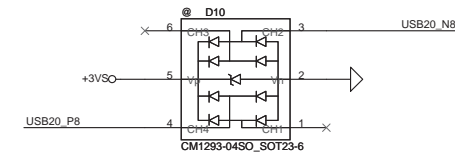
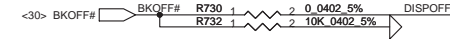
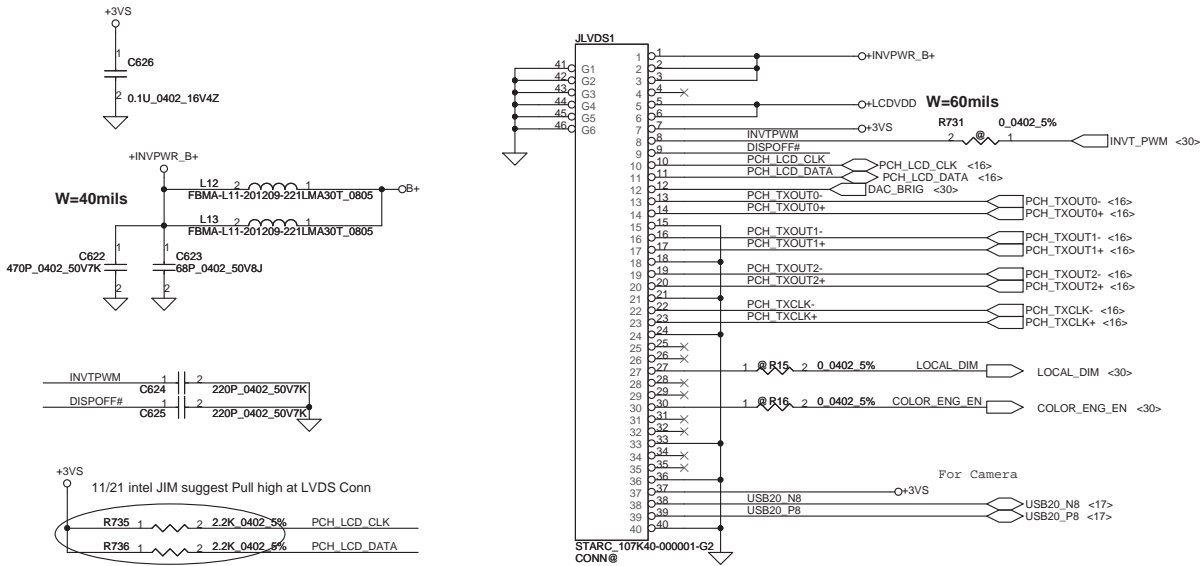


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LCD POWER CIRCUIT

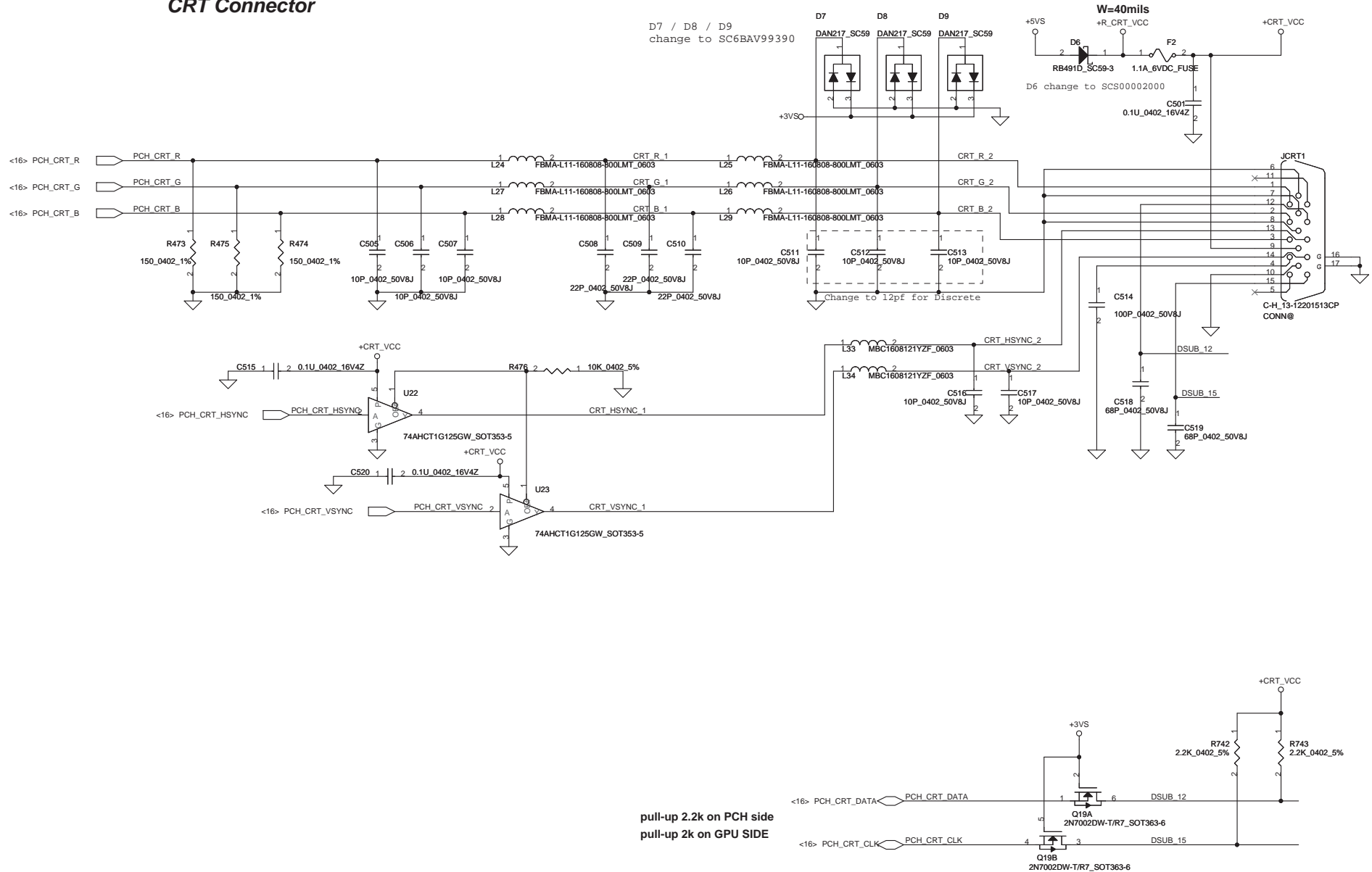


LED PANEL Conn.



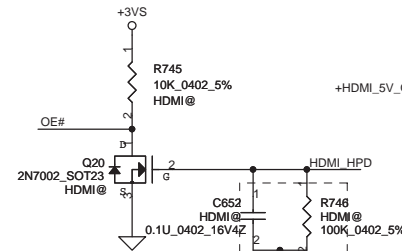
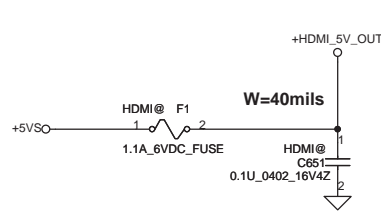
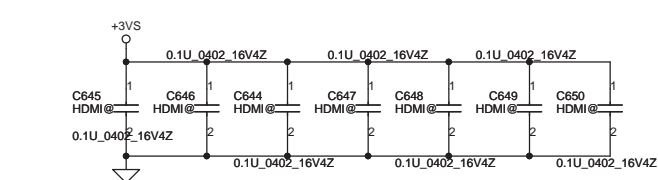
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										Document Number		Rev	
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										Date:		Friday, July 02, 2010	

CRT Connector

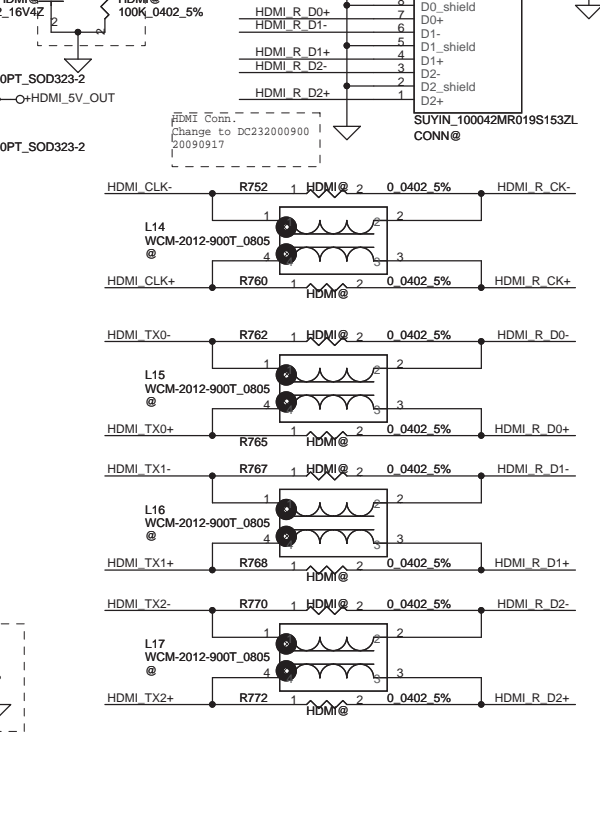
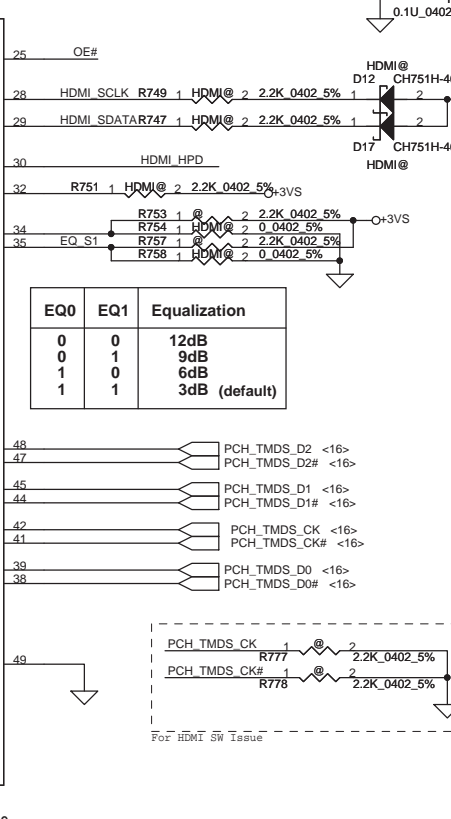
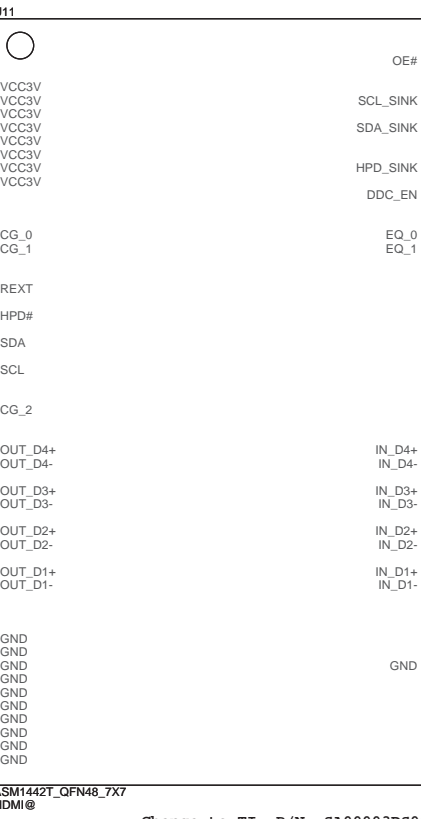
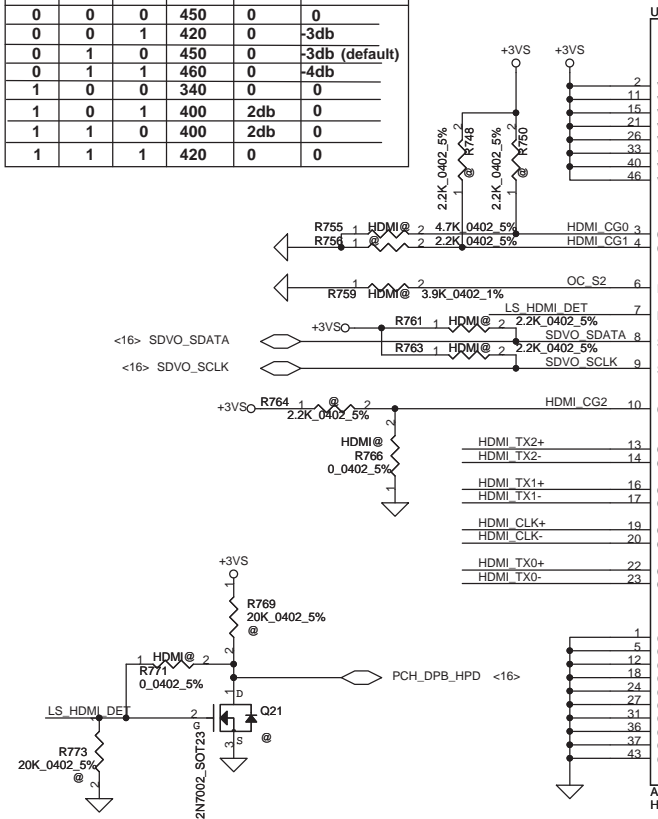
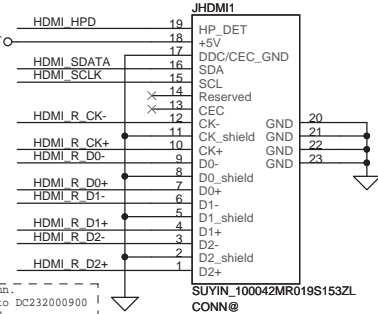


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CG0	CG1	CG2	Swing	Pre-amp	Slew-rate
0	0	0	450	0	0
0	0	1	420	0	-3db
0	1	0	450	0	-3db (default)
0	1	1	460	0	-4db
1	0	0	340	0	0
1	0	1	400	2db	0
1	1	0	400	2db	0
1	1	1	420	0	0

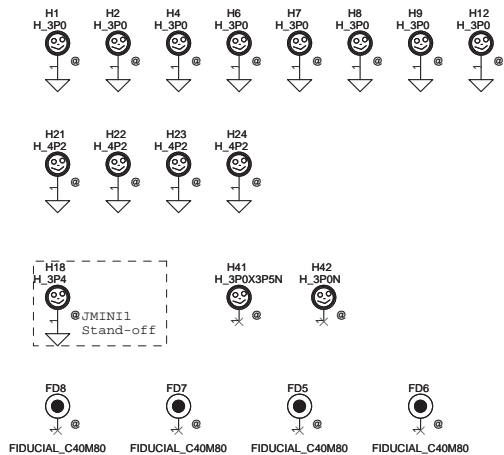


NAL00 HDMI connector

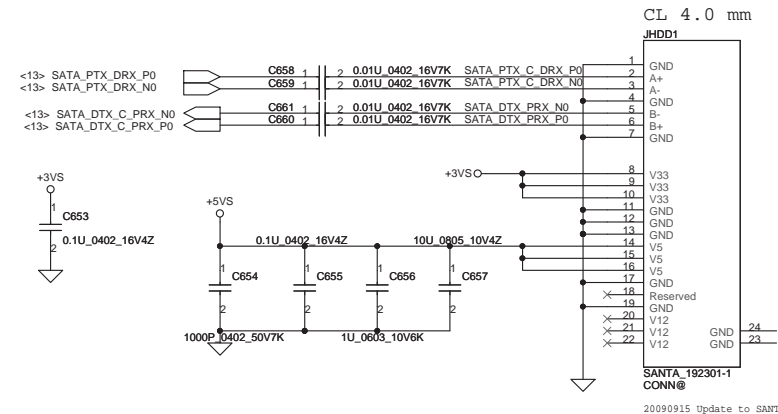


Change to TI P/N: SA00003DS00
20100608

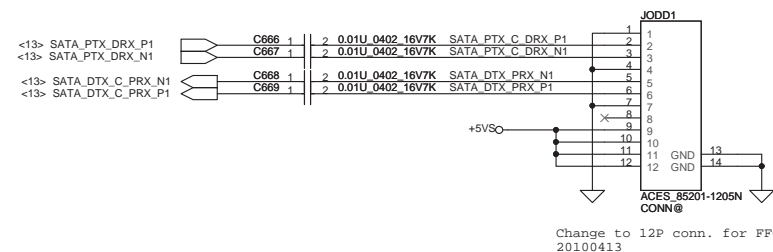
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SATA HDD1 Conn.

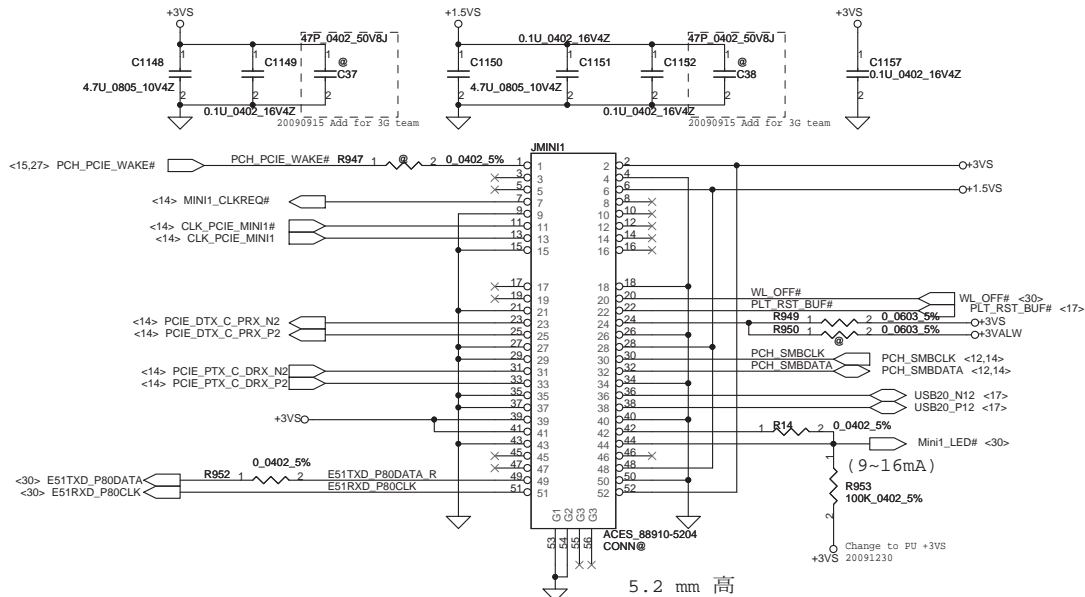


SATA ODD FFC Conn.



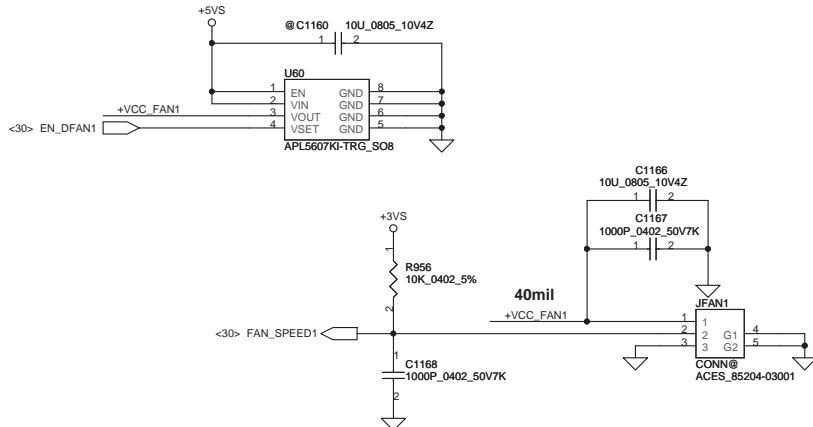
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For Wireless LAN



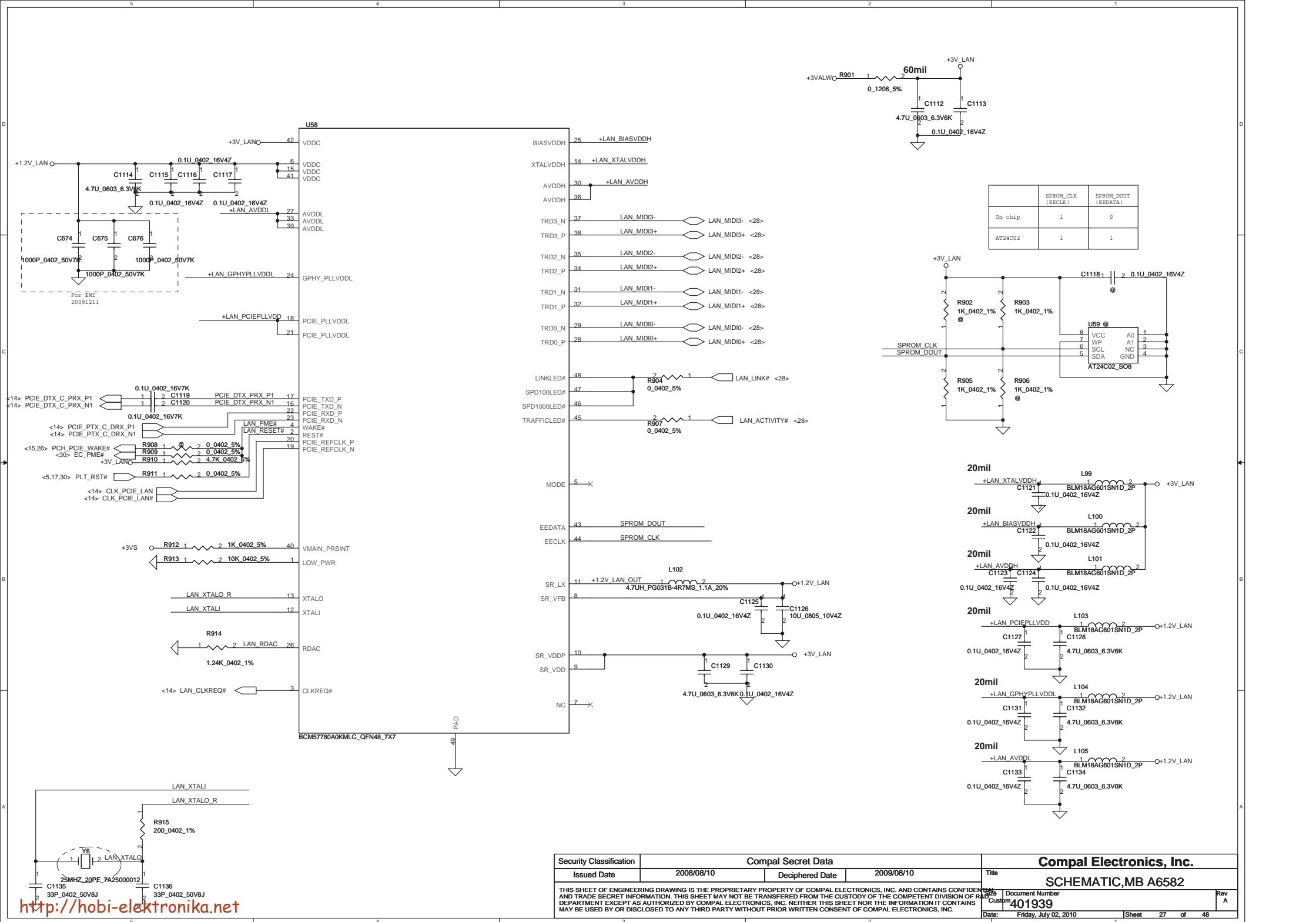
Mini Card Power Rating			
Power	Primary Power (mA)		Auxiliary Power (mA)
	Peak	Normal	Normal
+3VS	1000	750	
+3V	330	250	250 (wake enable)
+1.5VS	500	375	5 (Not wake enable)

FAN1 Conn

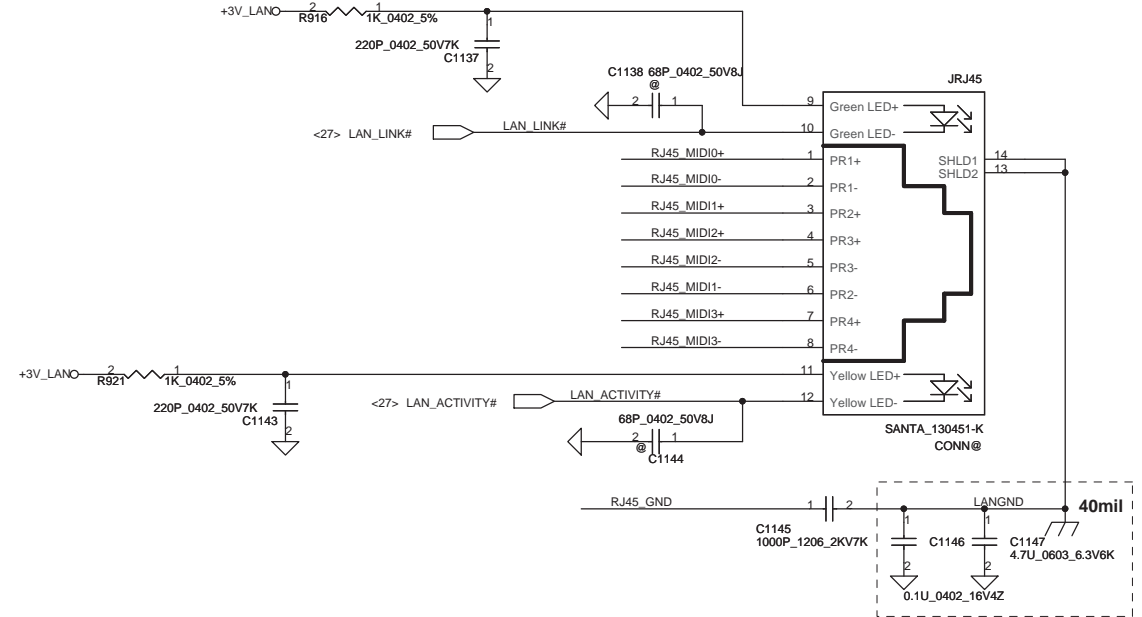
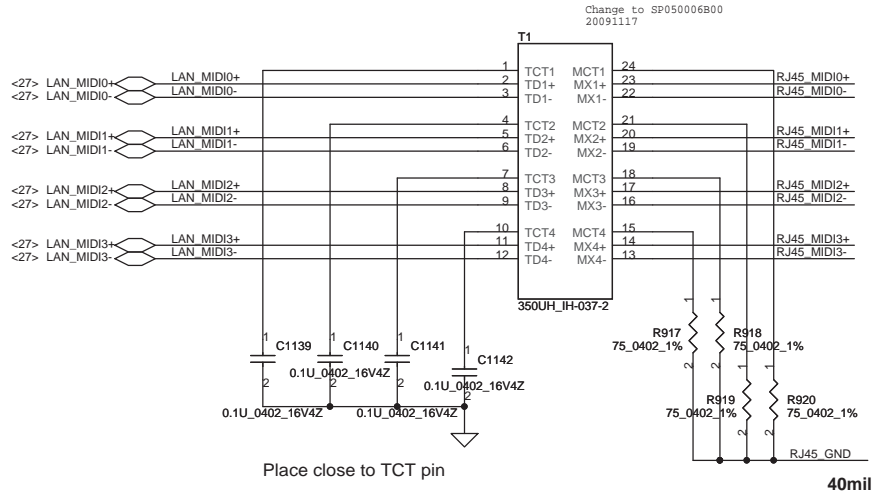


Del 3G / GPS Module Connect

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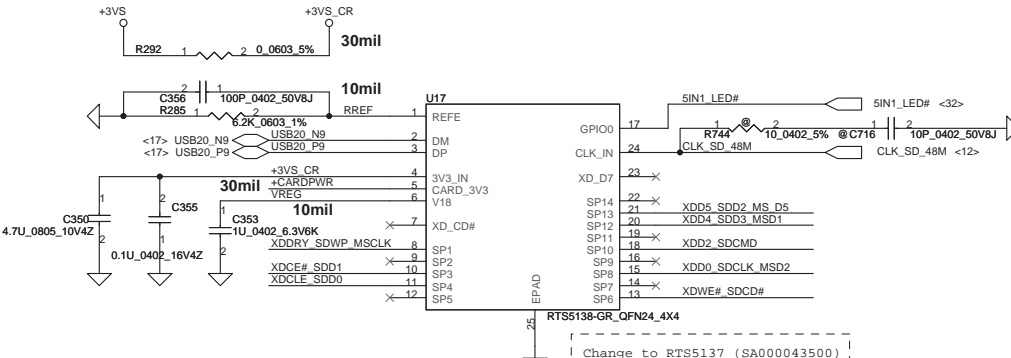


LAN Connector

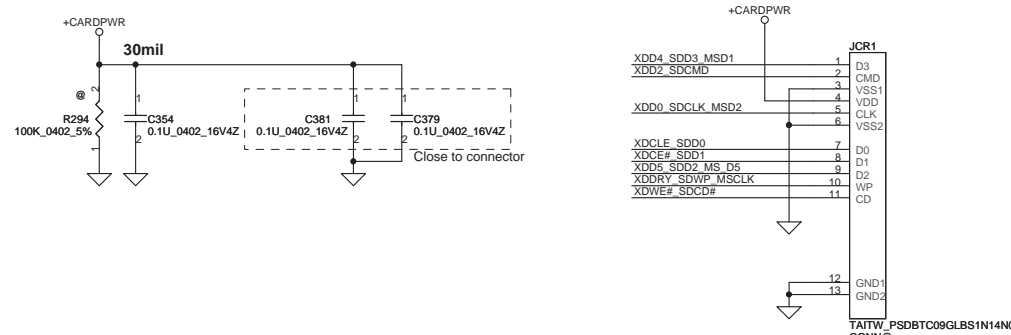


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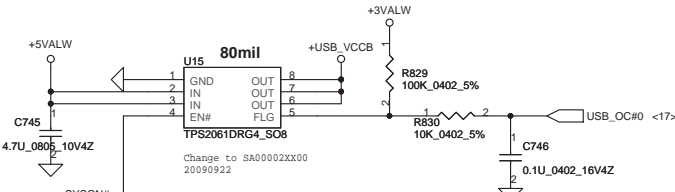
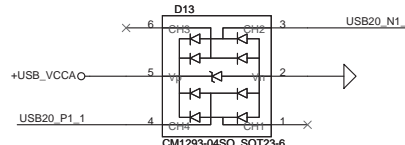
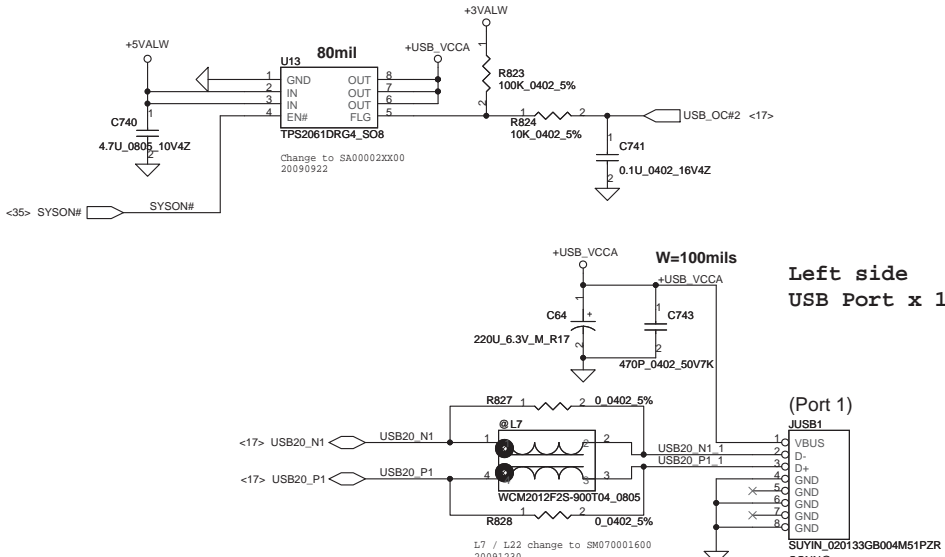
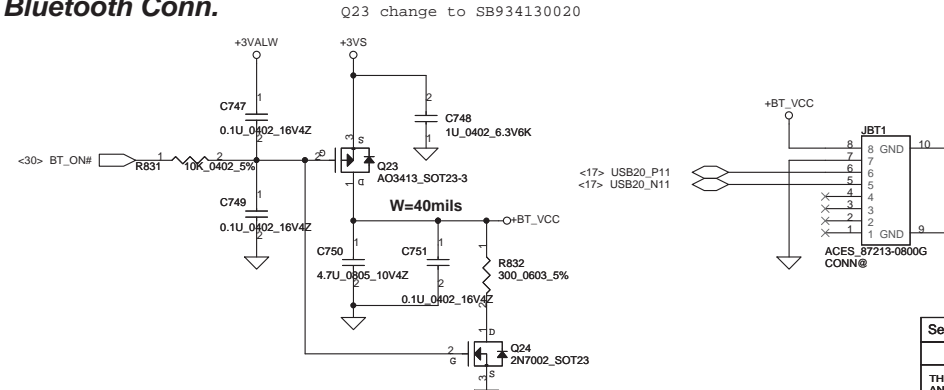
Card Reader RTS5138 / RTS5137
(only SD+MMC function)



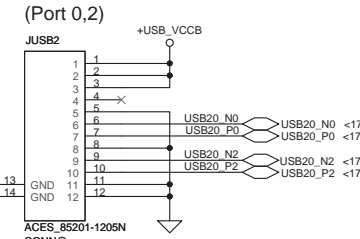
Card Reader Connector



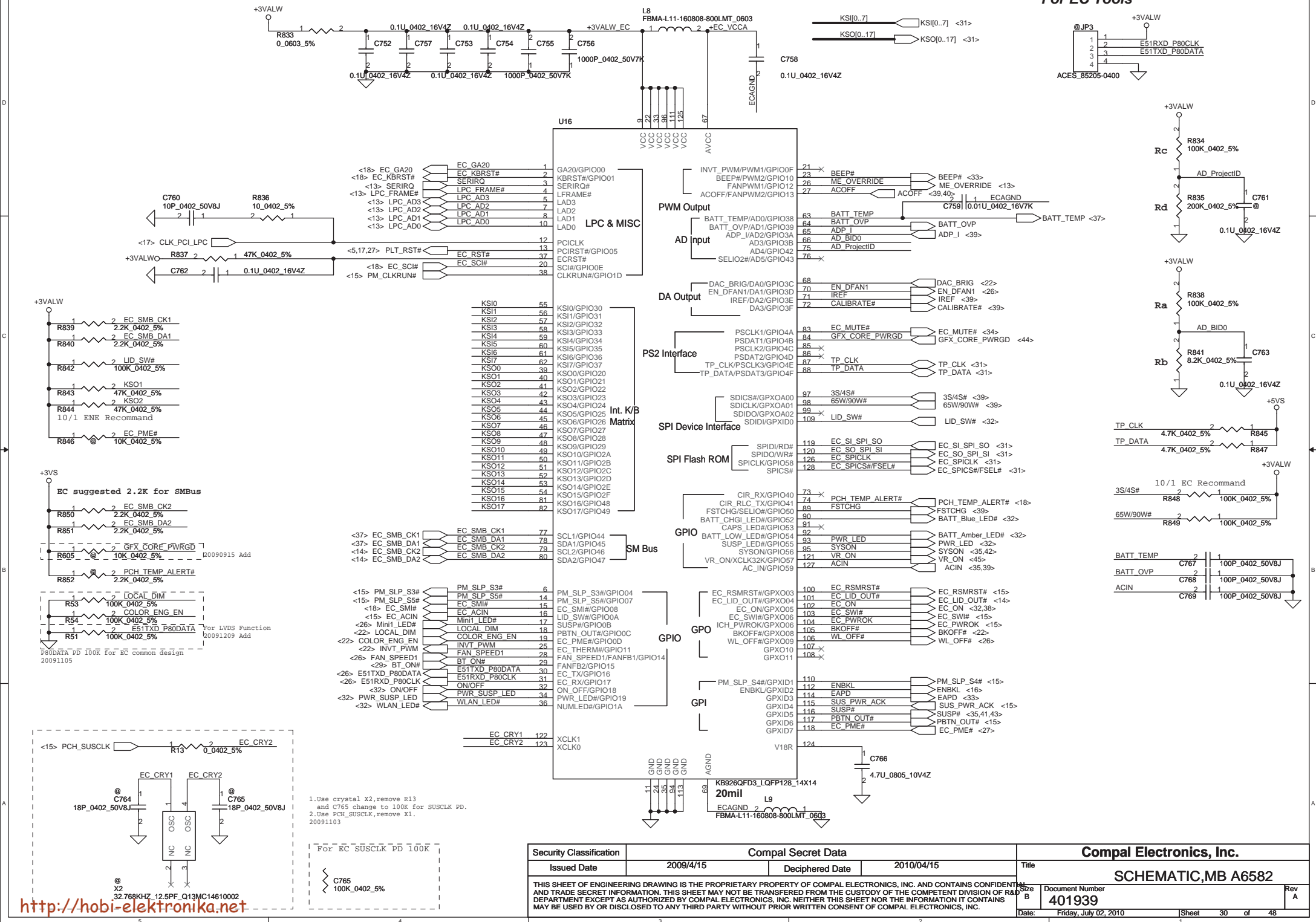
Bluetooth Conn.

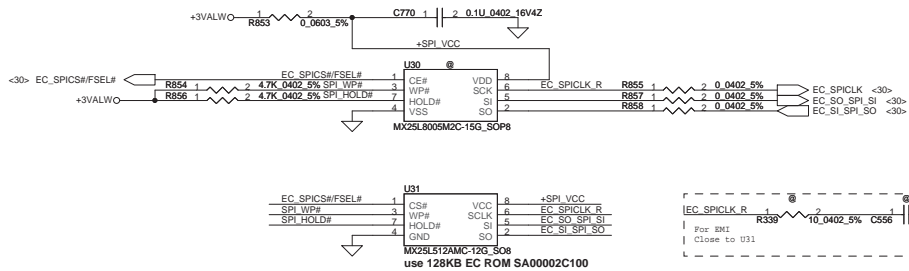
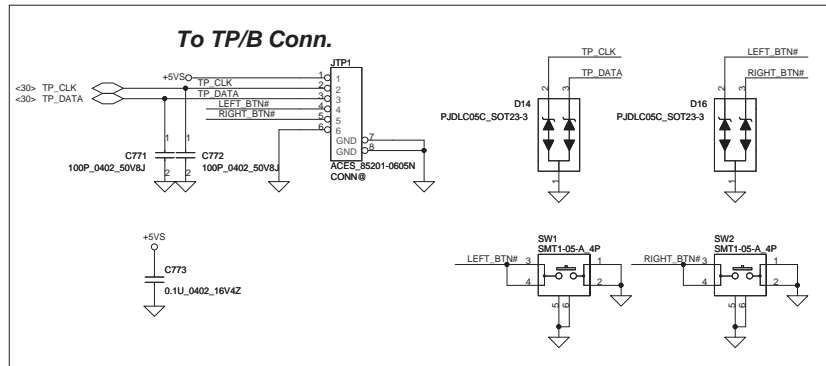
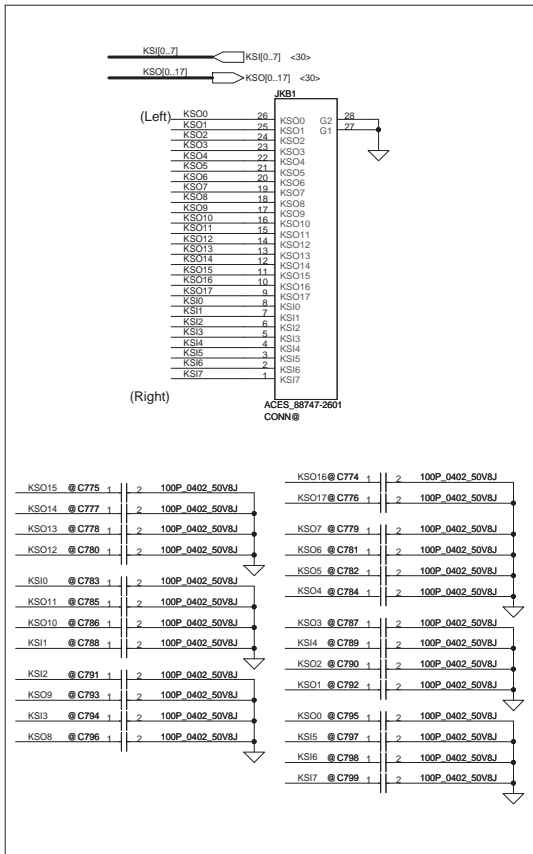


USB/B Conn.



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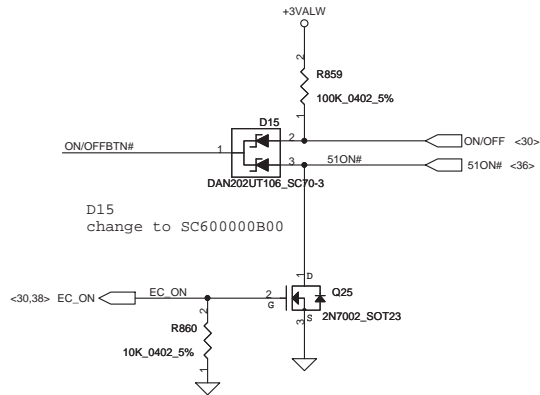


ENE suggestion SPI Frequency over 66MHz
 SST: 50MHz
 MXIC: 70MHz
 ST: 40MHz

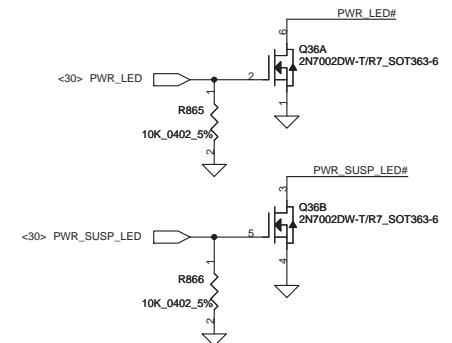
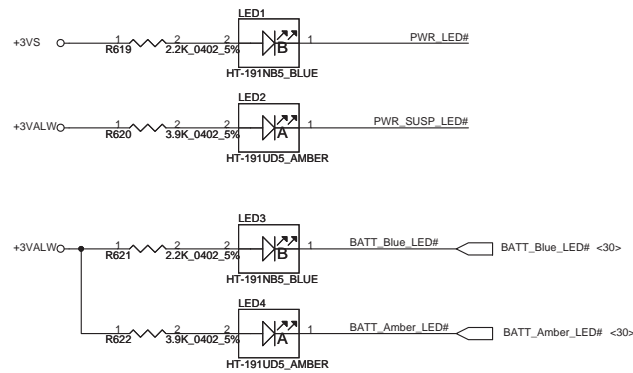
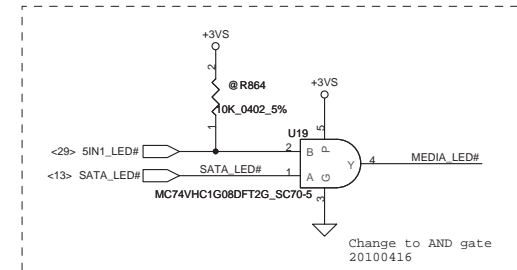
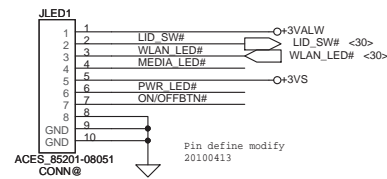
To BTN/B Conn.

KSO0	KSO3
KSI1	WL_BTN#
KSI2	T/P lock_BTN#
KSI3	Back up_BTN#
KSI4	BT_BTN#
KSI5	Power save_BTN#

Power Button

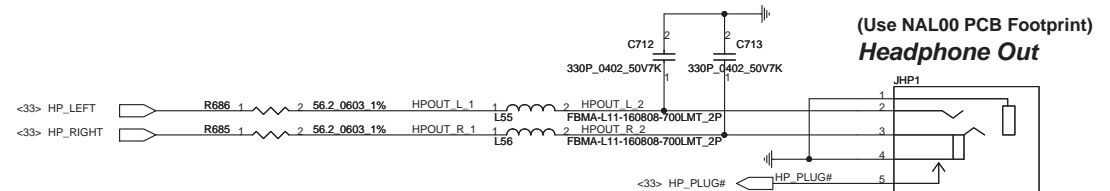
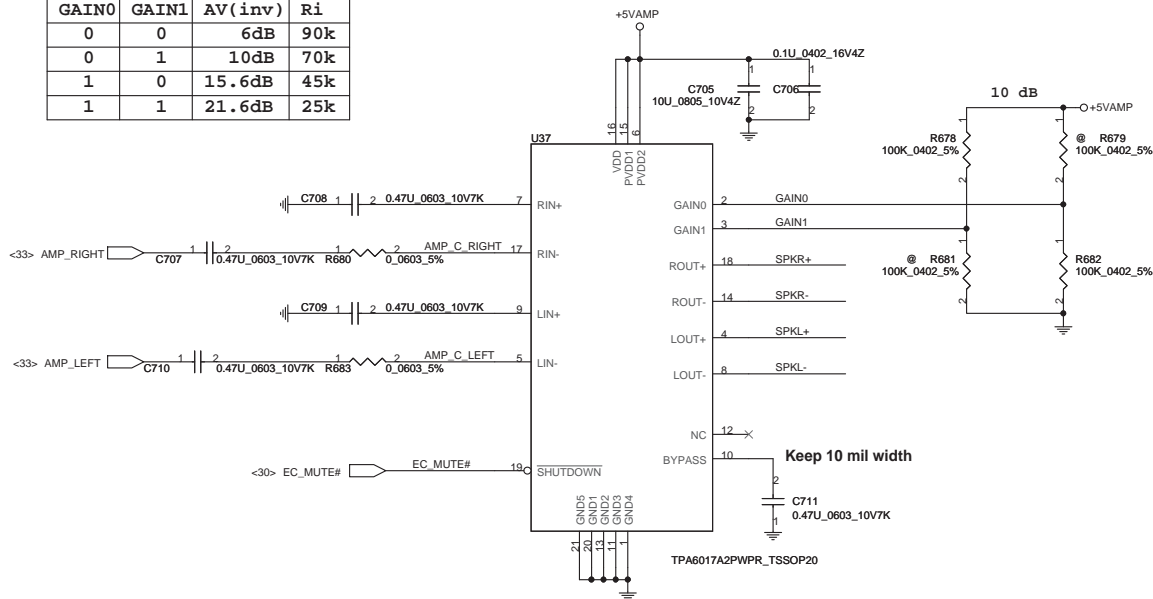


LED/B LEFT

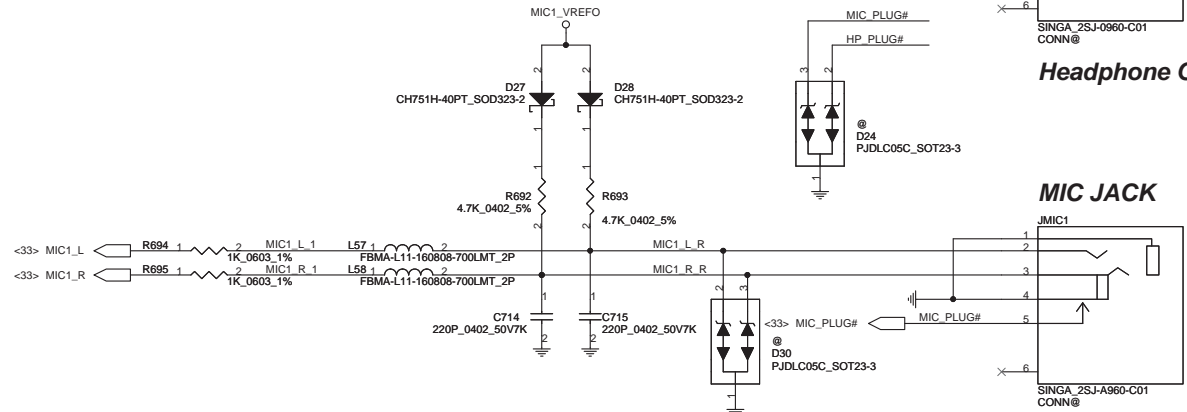
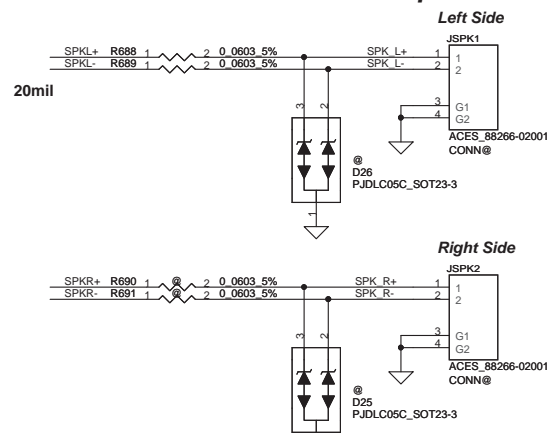


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GAIN0	GAIN1	AV(inv)	Ri
0	0	6dB	90k
0	1	10dB	70k
1	0	15.6dB	45k
1	1	21.6dB	25k



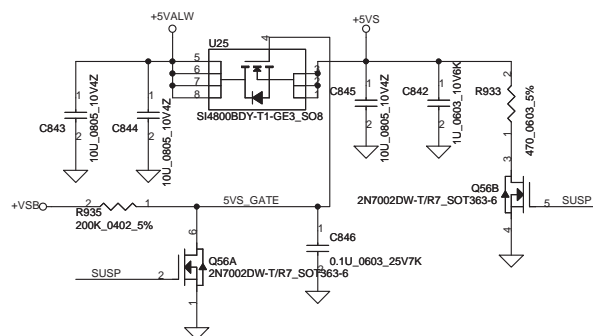
Int. Speaker Conn.



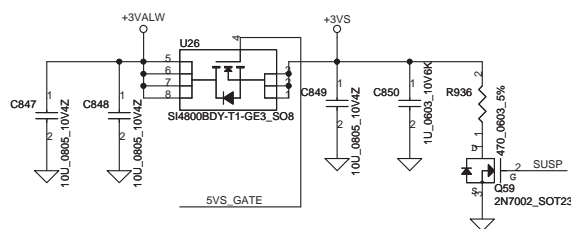
D25 / D26 Change to SCA00001100 for ESD 20090921

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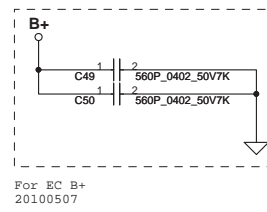
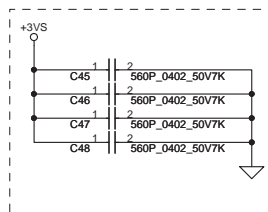
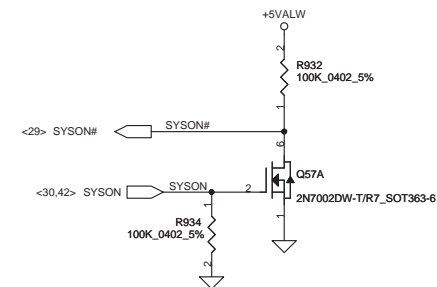
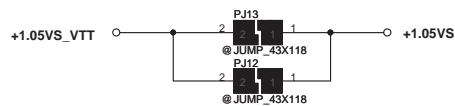
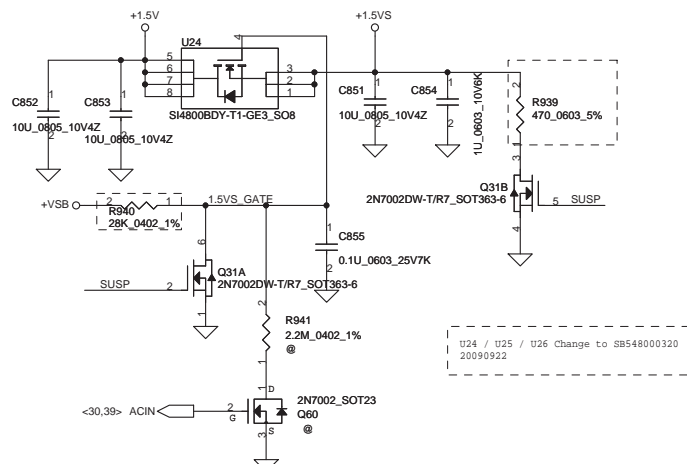
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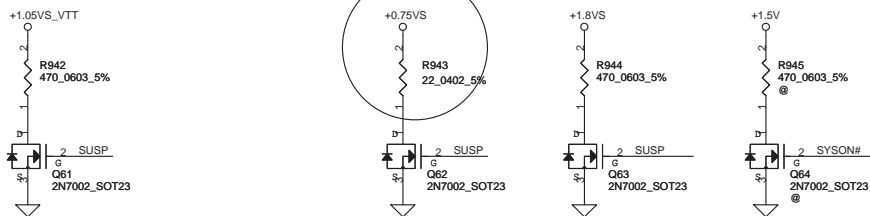
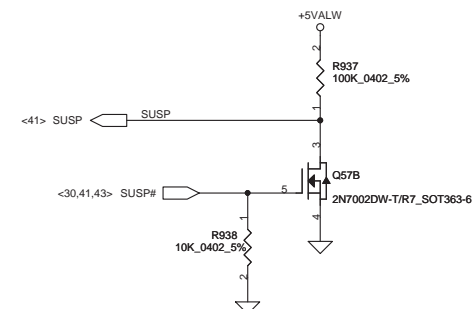
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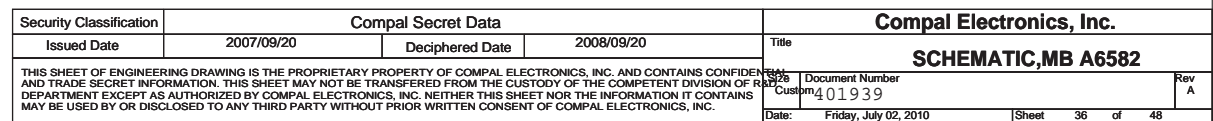
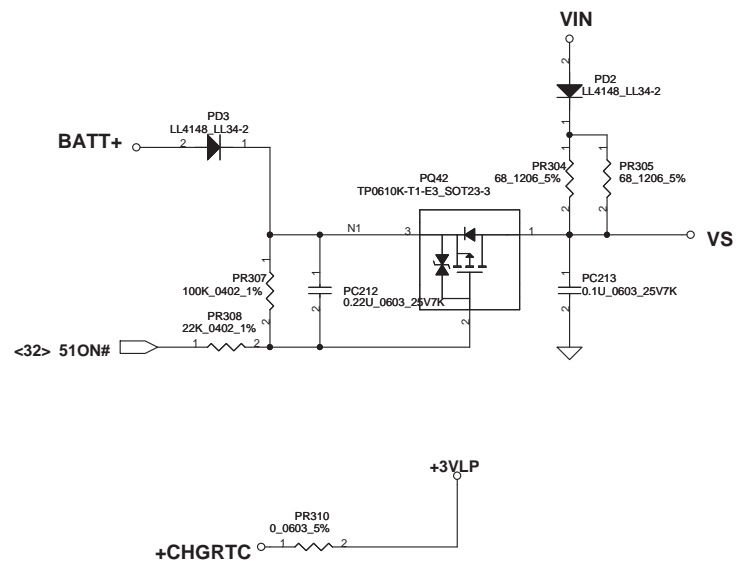
+1.5V to +1.5VS

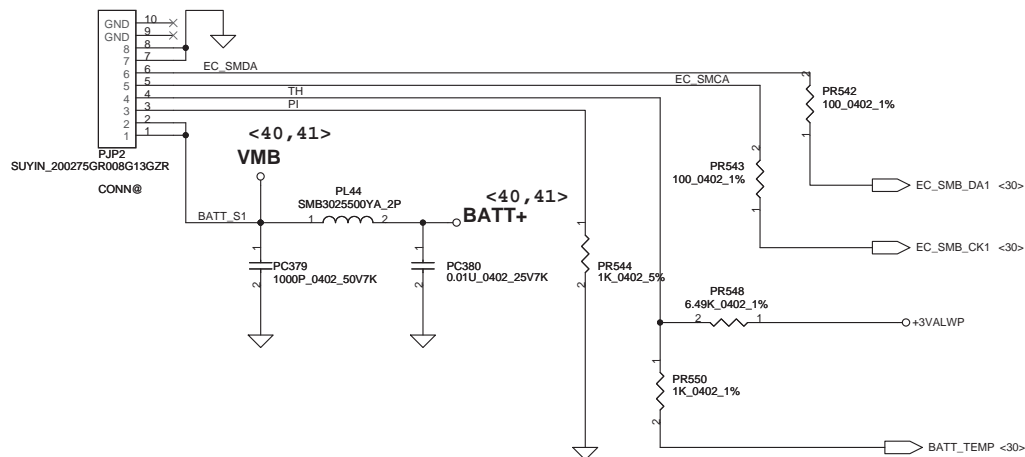


For LAM Common mode noise
200911102330

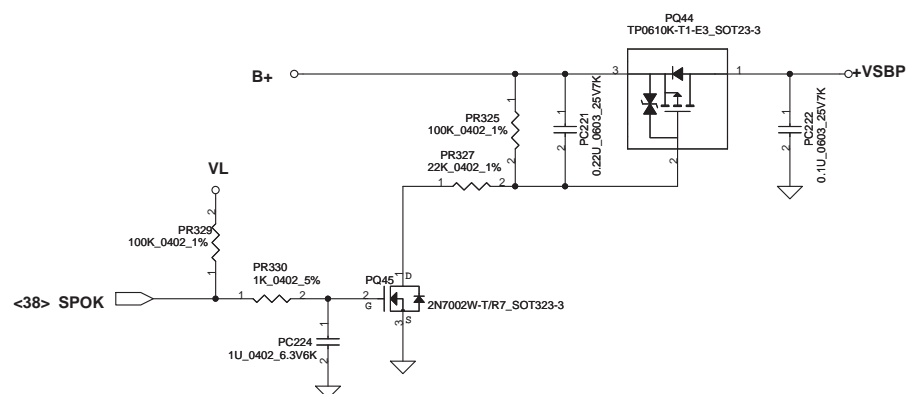
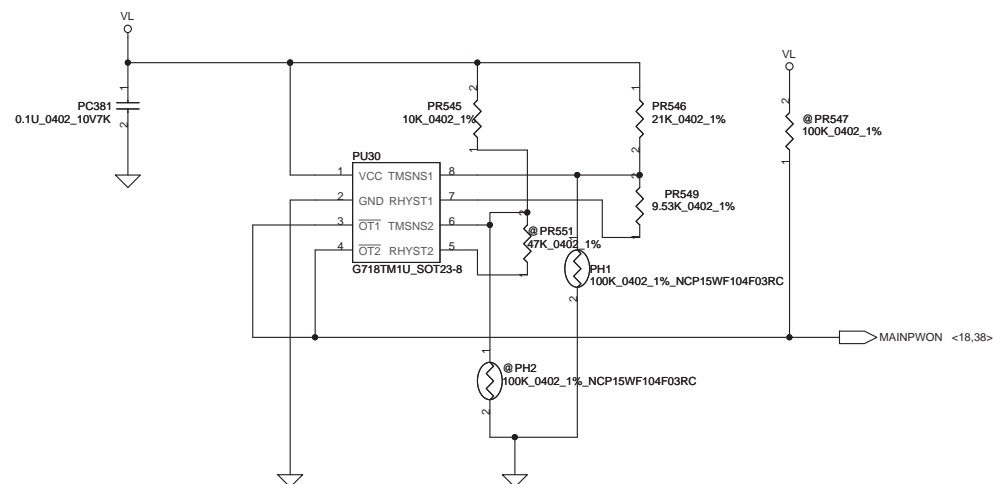


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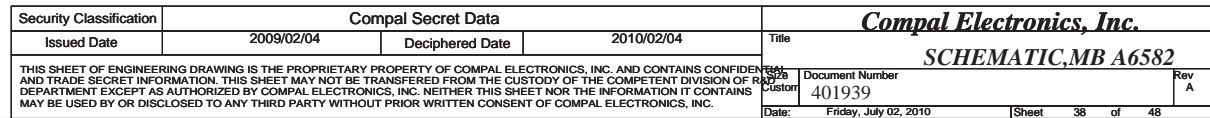


PH1 under CPU botten side :
CPU thermal protection at 92 degree C
Recovery at 56 degree C



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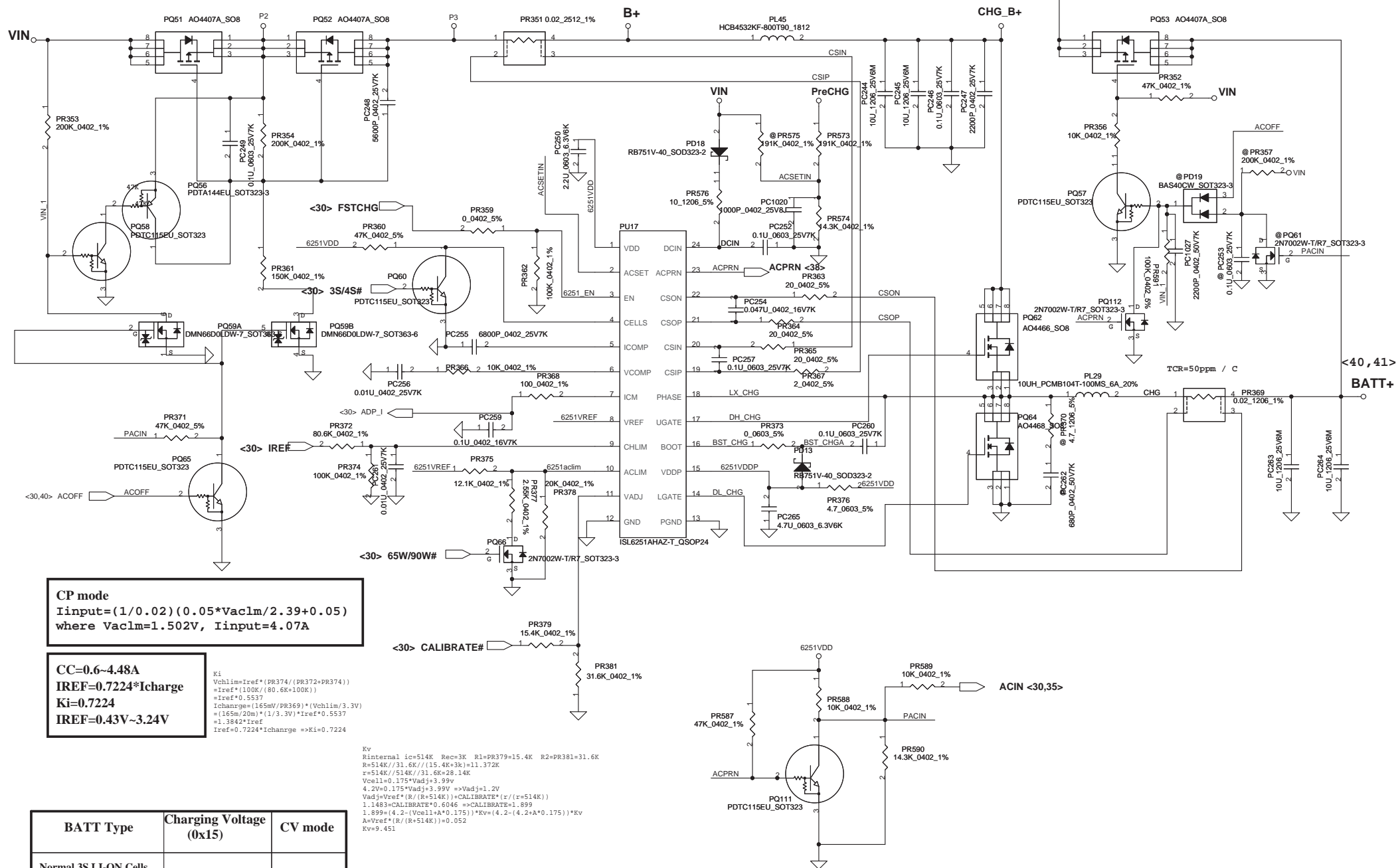
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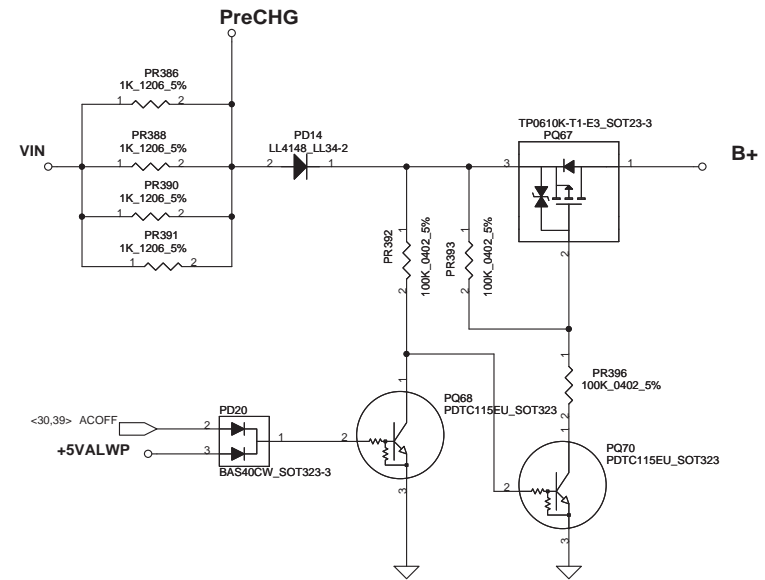


Iada=0~4.74A(90W/19V=4.736A)
Iada=0~3.42A(90W/19V=3.421A)

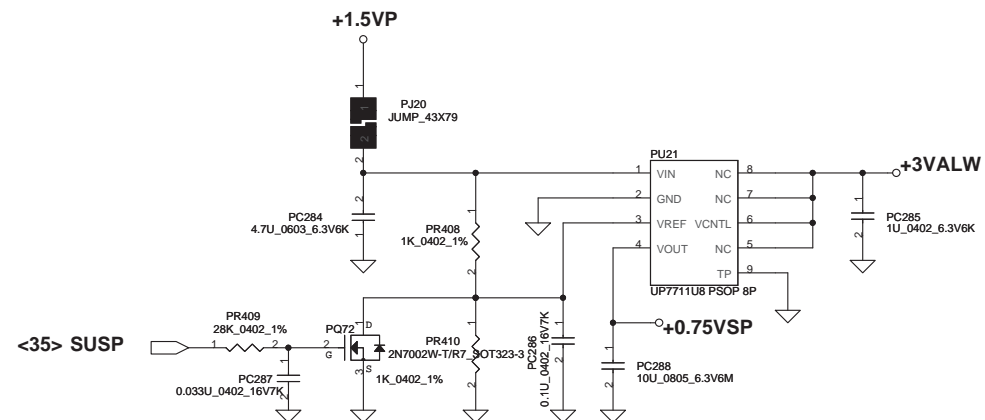
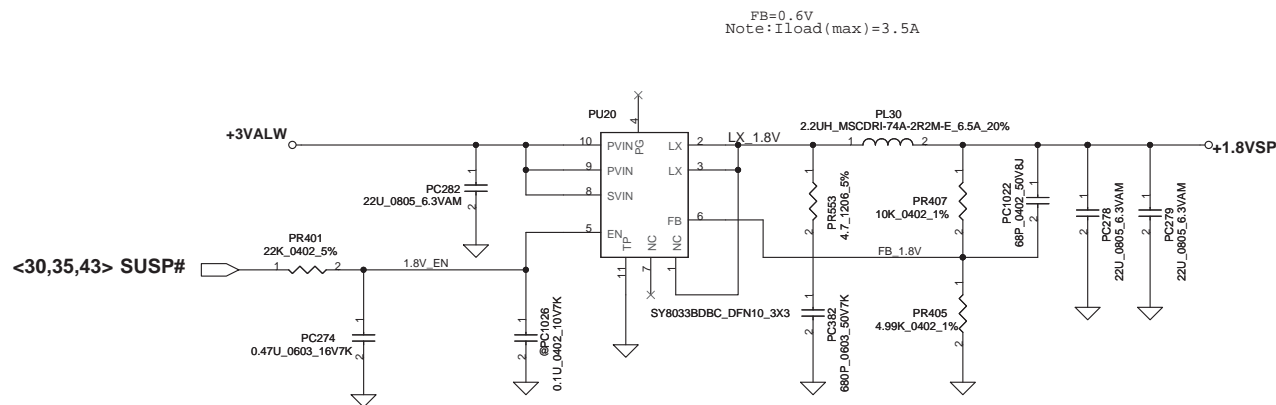
ADP_I = 19.9*Iadapter*Rsense

CP = 85%*Iada ; CP = 4.07A
CP = 85%*Iada ; CP = 2.91A

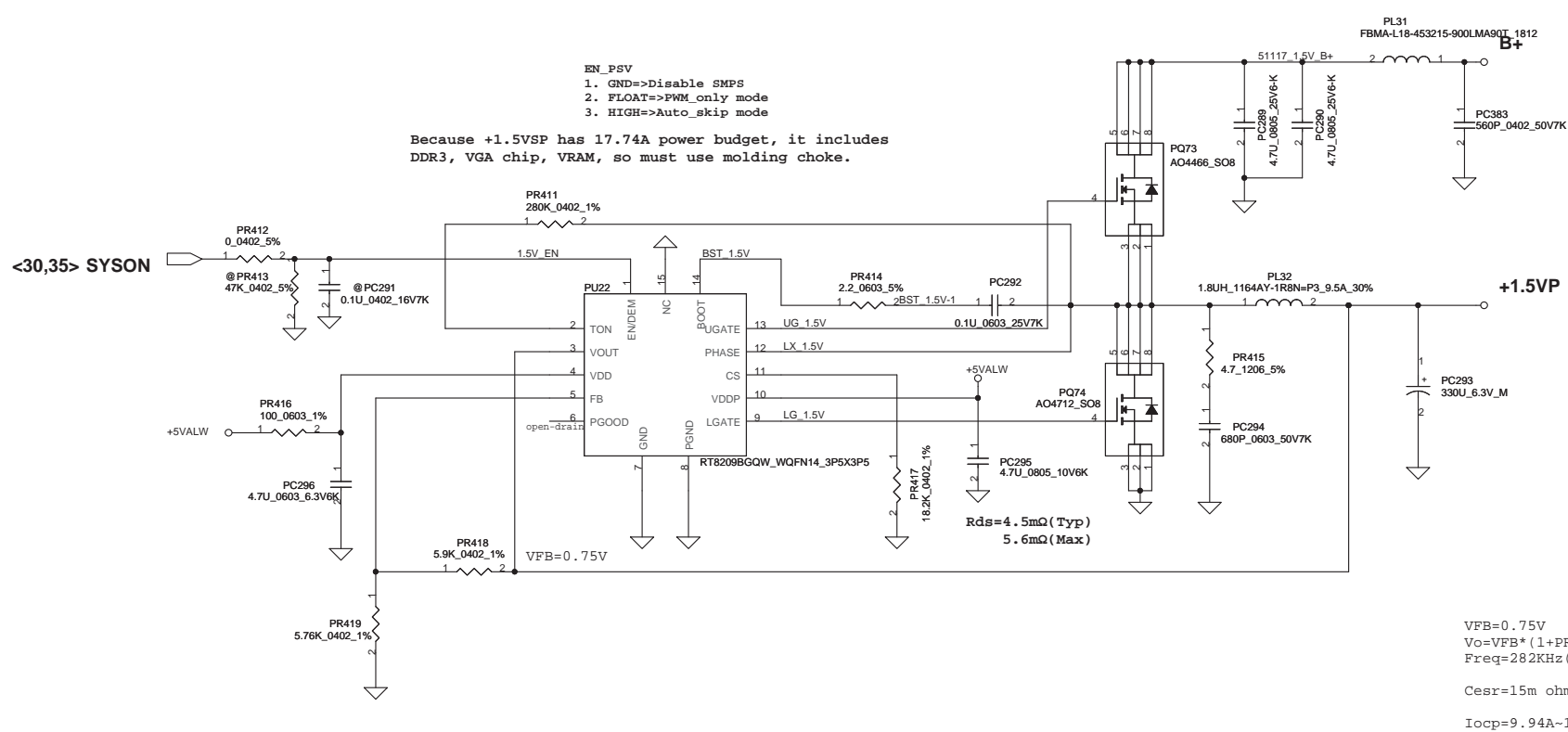




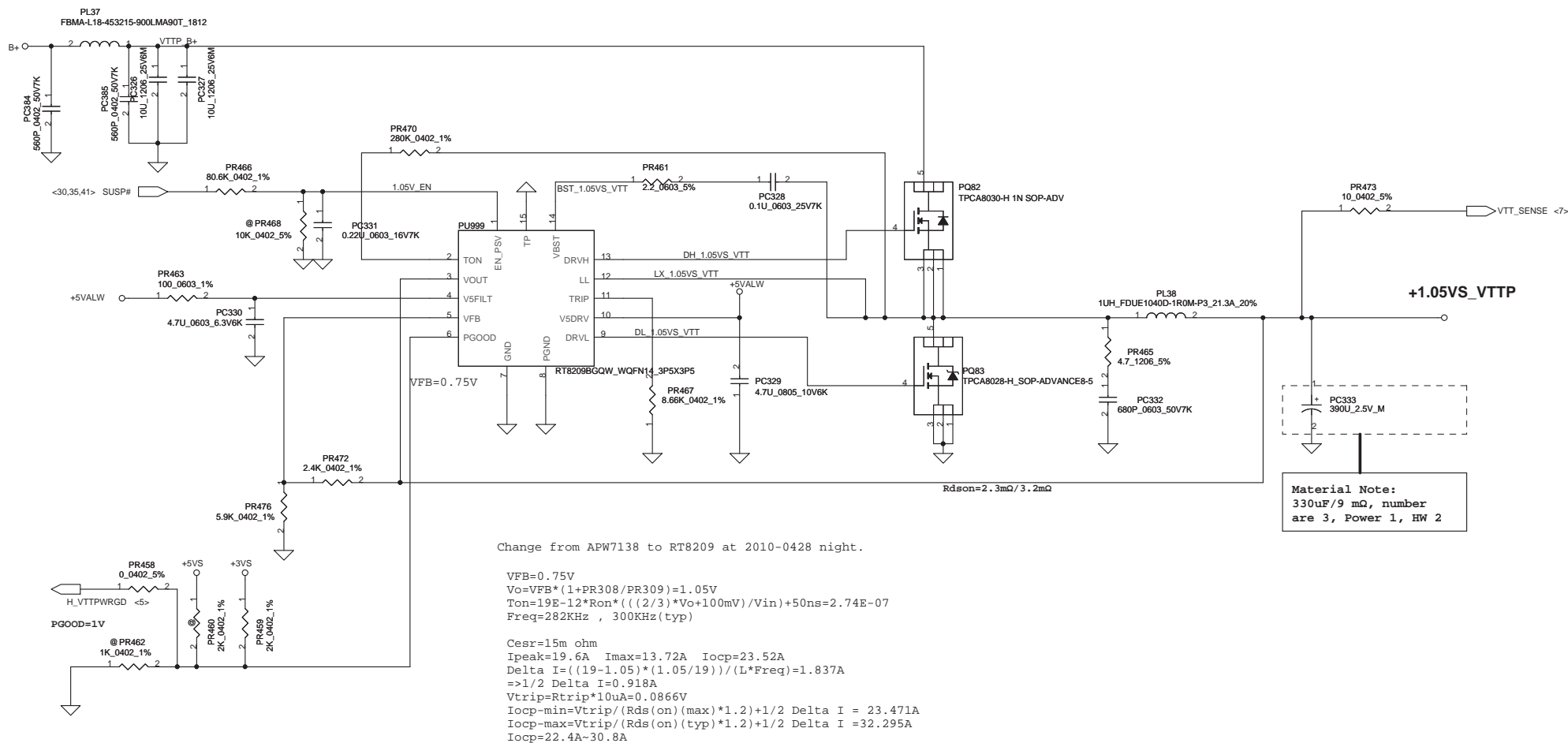
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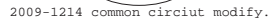


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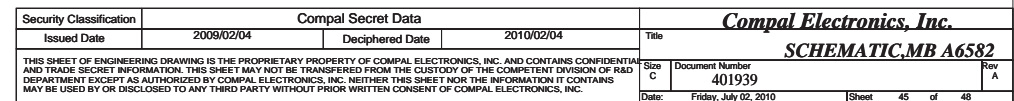
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where $R_{droop}=PR271=8.66k\text{ ohm}$, $R_i=PR283=1.69k\text{ ohm}$
 $I_{ocp}=OCF\text{ Threshold}\cdot R_{droop}/LL=24.89A$



<http://hobi-elektronika.net>

	HFM_VID	HFM_Icc	LL	Icc_TDC	Icc_Dy
Auburndale 45W	1.075	50	1.9m	37	35
Auburndale 35W	0.975	38	1.9m	29	27
Clarksfield SV	0.95	51	1.9m	38	39
Clarksfield XE	0.95	65	TBD	48	TBD



Version change list (P.I.R. List)

Page 1 of 2 for PWR

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
1					Change PR353 from SD034470280 to SD034200380.		
2	Pre-Charge circiut update.	Pre-Charge circiut disable in EVT, now modify and enable it.	0.1	39	Change PR371 from SD034220280 to SD034470280. Change PR587 from SD034100380 to SD034470280. Change PR590 from SD034200280 to SD034143280. Change PR591 from SD028470280 to SD028100380.	2010/06/08	to PVT
3					Add PC1027 SE074222K80(S CER CAP 2200P 0402 50V7K) Change PQ111 from SB000006800 to SB301150200		
4	Pre-Charge circiut update.	Pre-Charge circiut disable in EVT, now modify and enable it.	0.1	38	Add PQ102 and PQ103 SB301150200. Add PR592 SD034200380.	2010/06/08	to PVT
5	Pre-Charge circiut update.	Pre-Charge circiut disable in EVT, now modify and enable it.	0.1	40	Change PR386 from SD011000080 to SD011100180. Add PR388, PR390, PR391 SD011100180. Add PD14 SC100001Y80 LL4148_LL34-2	2010/06/08	to PVT
6	EMI fail, add 3/5V snubber and Boost Resister to silve it.	EMI request to solve EMI issue.			Add PR566 and PR567 SD001470B80 S RES 4.7 1206 5%		
7			0.1	38	Add PC1012 and PC1013 SE074681K80 S CER CAP 680P 0402 50V7K	2010/06/08	to PVT
8	EMI fail, add 1.5V snubber.	EMI request to solve EMI issue.	0.1	42	Change PR564 and PR565 from SD013000080 to SD013220B80 Add PR415 SD001470B80 4.7 1206 5%. Add PC294 SE025681K80 S CER CAP 680P 50V K X7R 0603 Add PR465 SD001470B80 4.7 1206 5%.	2010/06/08	to PVT
9	EMI fail, add 1.05V snubber. and Boost Resister.	EMI request to solve EMI issue.	0.1	43	Add PC332 SE025681K80 S CER CAP 680P 50V K X7R 0603	2010/06/08	to PVT
10					Change PR461 from SD013000080 to SD013220B80 Add PR268 SD001470B80 4.7 1206 5%.		
11	EMI fail, add GFX_CORE snubber.	EMI request to solve EMI issue.	0.1	44	Add PC199 SE025681K80 S CER CAP 680P 50V K X7R 0603	2010/06/08	to PVT
12	HW power sequence modify.	HW request.	0.1	43	Change PR466 from SD034576280 S RES 1/16W 57.6K +-1% 0402 to SD034806280 S RES 80.6K 0402 1%) Change PC331 from SE076104K80 S CER CAP .1U 16V K X7R to SE00000R700 S CER CAP 0.22U 16V K X7R 0402	2010/06/08	to PVT
13							
14	BOM unique.	BOM unique.	0.1	38	Chnage PQ101 from SB301150000 to SB301150200.	2010/06/08	to PVT
15	EMI request.	EMI request.	0.1	38	Add PC1028 SE042104K80 S CER CAP .1U 25V K X7R 0603	2010/06/08	to PVT
16	EMI request.	EMI request.	0.1	44	Add PC1029 SE042104K80 S CER CAP .1U 25V K X7R 0603	2010/06/08	to PVT
17	Sourcer request to change a common part.	Sourcer request.	0.1	38	Change PC1019 from SE00000GC00 S CER CAP 2.2U 10V K X7R 0603 to SE000003H00 S CER CAP 2.2UF 10V K X5R 0603	2010/06/08	to PVT
18	Per sourcer request.	Sourcer request to change PC331 from SE00000R700 to SE026224K80 for common part.	0.1	43	change PC331 from SE00000R700 to SE026224K80	2010/06/08	to PVT
19	CPU transient issue.	Need modify PC362 to 330P due to transient fail.	0.1	45	Change PC362 from SE074561K80 S CER CAP 560P 50V K X7R 0402 to SE074331K80 S CER CAP 330P 50V K X7R 0402	2010/06/08	to PVT
20	Per sourcer request.	Per sourcer request.	0.1	43 44	Chnage PQ39/PQ82 from SB000008L80 to SB00000HL00.	2010/06/08	to PVT
21	Cost down.	Cost down 3VALWP and Charger Low Side MOS.	0.1	38 39	Change PQ98 from SB00000AJ00 to SB000009580(A04468). Change PQ64 from SB00000CG00 to SB000009580(A04468).	2010/06/08	to PVT
22	Cost down.	Cost down +1.5VP Low side MOS and choke.	0.1	42	Chnage PQ74 from SB000009F80 to SB00000AJ00(A04712). CHnage PL32 from SH000009U00 to SH000009680.	2010/06/08	to PVT
23	Cost down.	re-caculate 1.5VP OCP.	0.1	42	Change PR417 from SD034110280 to SD034182280.	2010/06/08	to PVT

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24	Cost down.	re-caculate +3VALWP OCP.	0.1	38	Change PR562 from SD034107380 to SD034137380.	2010/06/08	to PVT
25							
26							
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Item	Phase	PAGE	DATE	Modifycatio list	Purpose
1	EVT		04 / 08 04 / 16 04 / 21 04 / 26 04 / 27 04 / 29 04 / 29 05 / 03 05 / 03 05 / 06 05 / 07 05 / 10 05 / 11 05 / 12 05 / 13	N/A Modify RTC SCH Change LAN to AR8152 Update Power SCH Update AND Gate symbol - U1 / U2 / U6 / U7 / U19 Update Power MOS symbol - U24 / U25 / U26 C196 change to 1U for INTEL design CLK GEN del C35 / C36 / C30 / C31 / C32 / C33 / C34 / L3 (for 3G@ and @ function) Q4+Q6 change to 2N7002DW Update U8 symbol Add DMIC function Modify USB define. Update Power SCH Add R17 for DMIC power Update Power SCH Change LAN to 616A&10/100 co-lay Del R307 (LVDS conn.) Change +1.05VS_VTT to +1.05VS (CLK GEN) Change C842 / C850 / C854 / C656 to SE080105K80 1U_0603_10V6K Change R940 to 28K (S3 Power sequence) SW T1 pin define (LAN) Change R907 (0 0603) to 0 1206 5% Remove C40 / C41 / C42 / C43 / C44 Change FAN Conn. Update Power SCH Change LAN to BCM57780 616A Del R637 / R639 / R640 / R645 / R647 / R648 (AUDIO) Del DMIC Update Power SCH Update Power SCH Add C49 / C50 (EC B+) Update Power SCH C764 / C765 change to 18P 0402_50V8J (EC) Reserve D13 / D24 / D30 (ESD) U17 change to RT55137 (SA000043500) Card reader Update Power SCH	
			06 / 01 06 / 02 06 / 03 06 / 04 06 / 10	Unpop L11 / C247 & Pop R304 for cost down Unpop C764 / X2 & Pop R13 & C765 Change to 100K for EC remove Crystal Pop R834 & R835 change to 200K for Project ID Unpop D24 / D30 Unpop R690 / R691 Pop C49 / C50 For HDMI Unpop R753 / R757 , Add R754 / R758 to 0 ohm. Change R759 to 3.9K. Change R755 to 4.7K ohm , Unpop R748. Unpop R778. R833 / R292 0 0805 change to 0 0603. C190 / C191 & C1135 / C1136 change to 33P 0402 50V8J for Vender test report Unpop C774~C799 for EMI cost down. Update Power SCH Add T25 / T26 / T27	